

Present and future of Photovoltaic Solar Electricity

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Introduction

Crystalline silicon technology, from quartz to system

Economical and environmental issues

Alternatives to crystalline silicon technology

Conclusions



Introduction

Crystalline silicon technology, from quartz to system

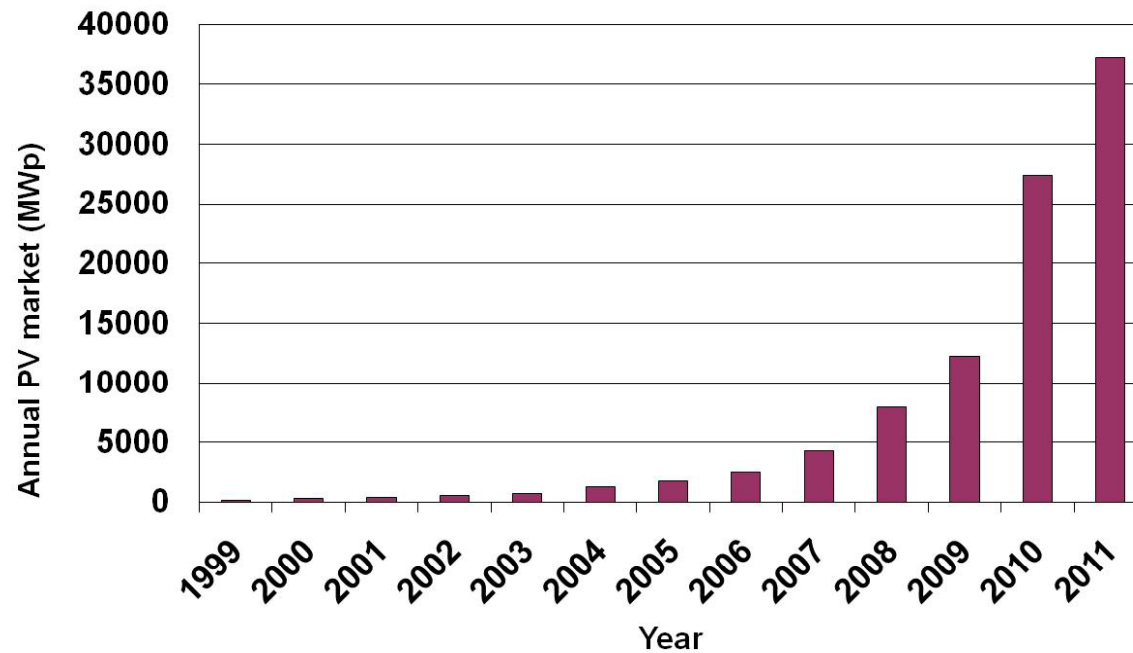
Economical and environmental issues

Alternatives to cristalline silicon technology

Conclusions

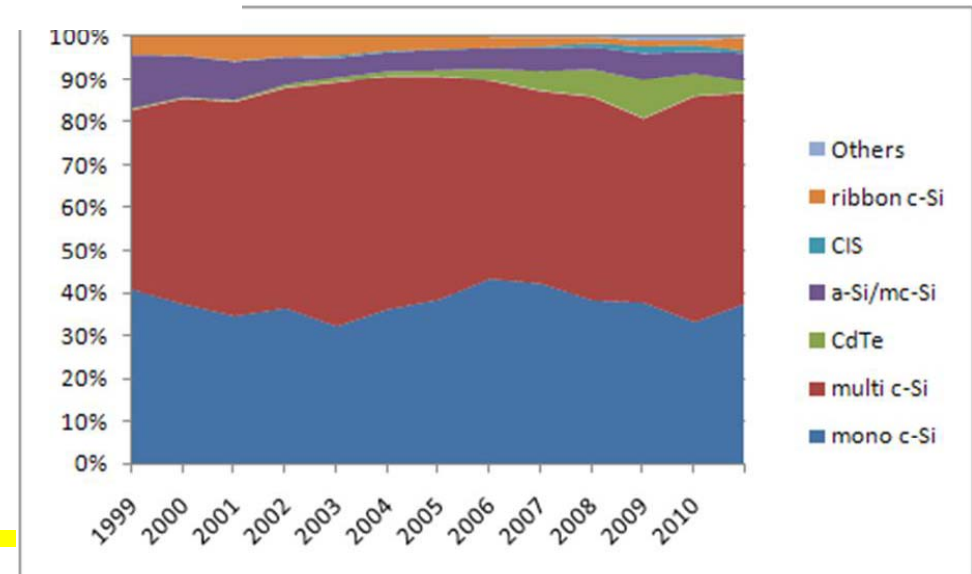


The photovoltaic (PV) industry



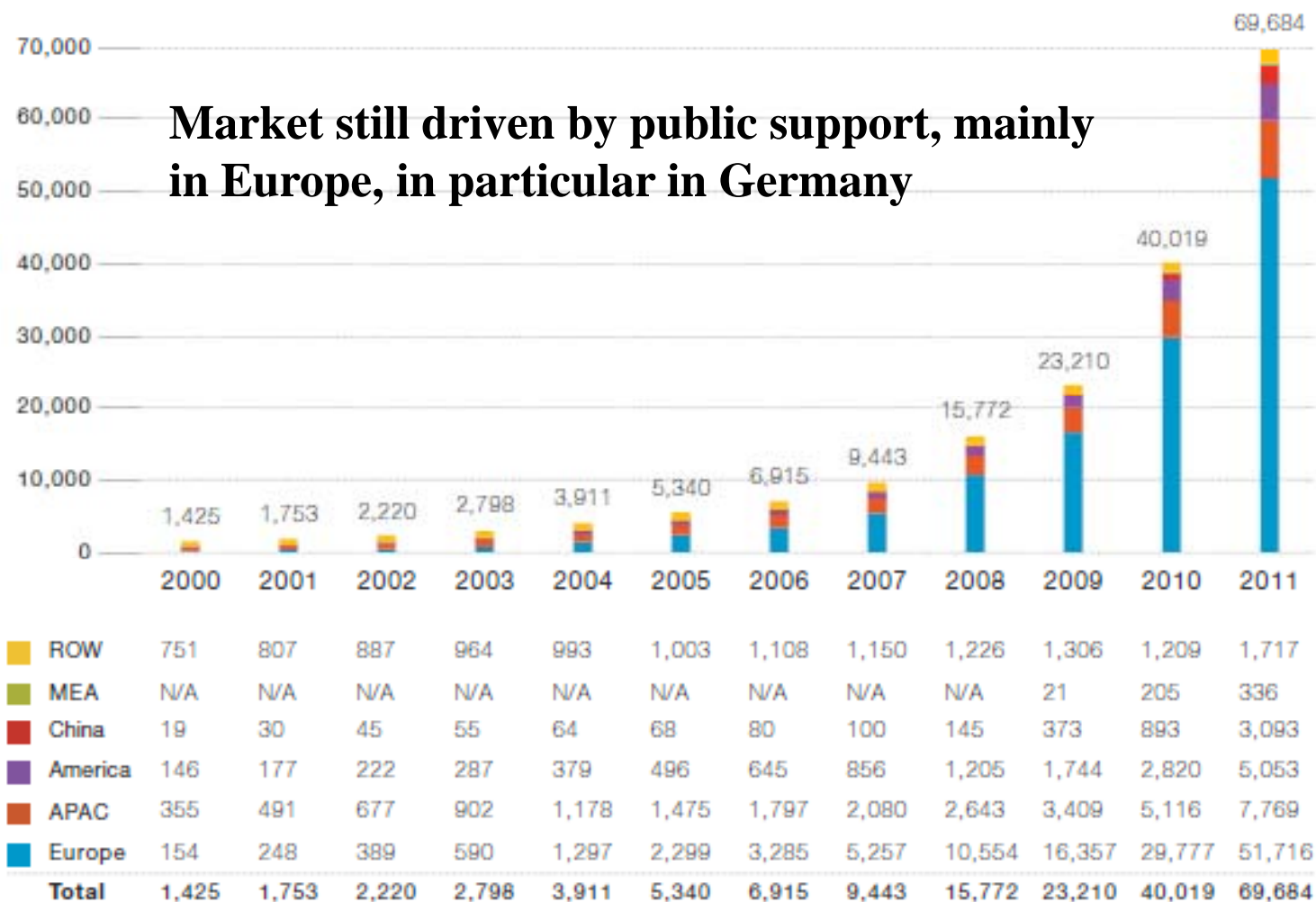
Continuous growth

Crystalline technology still dominant, but the share of other technologies is growing



PV market distribution

· Evolution of global cumulative installed capacity 2000-2011 (MW)

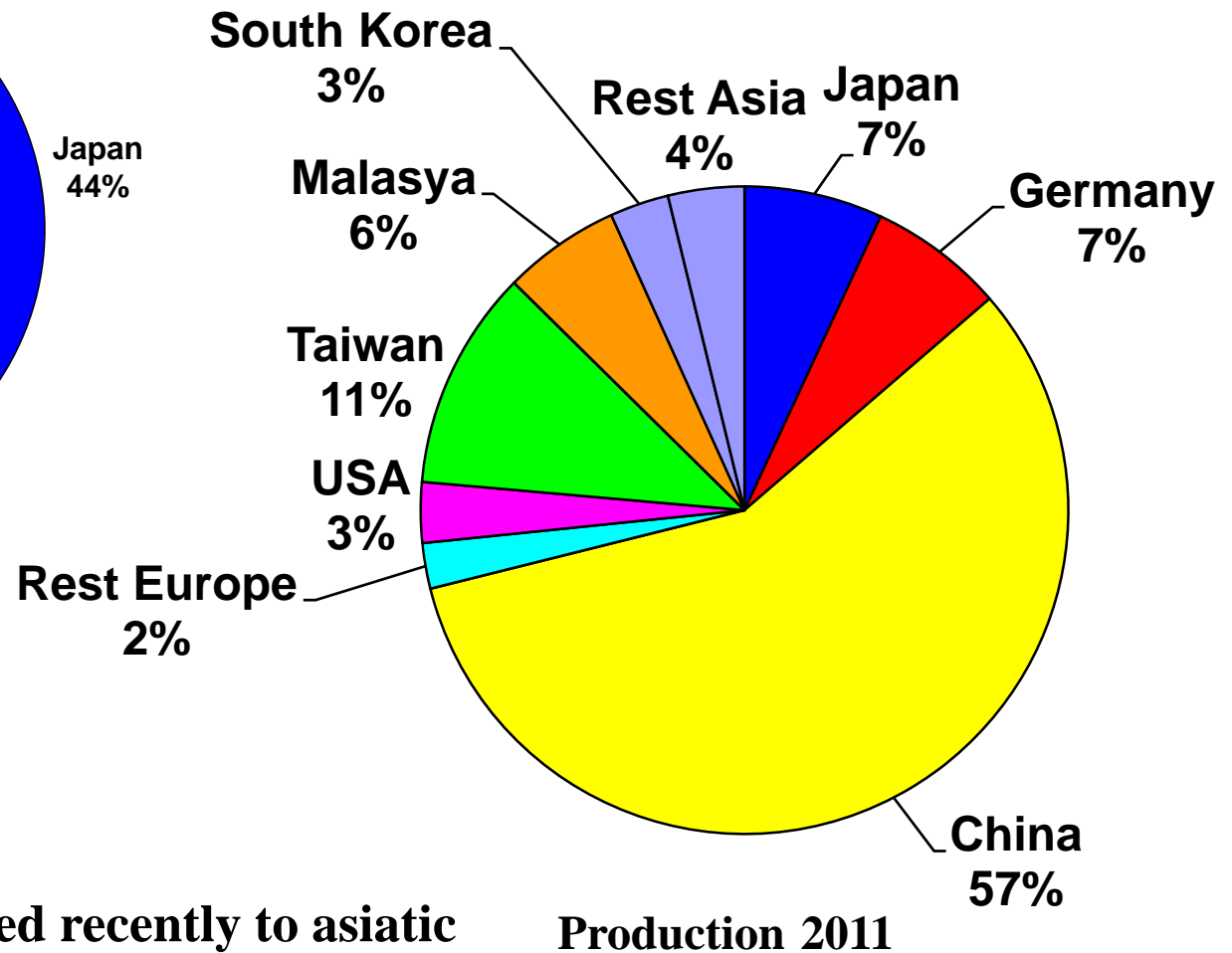
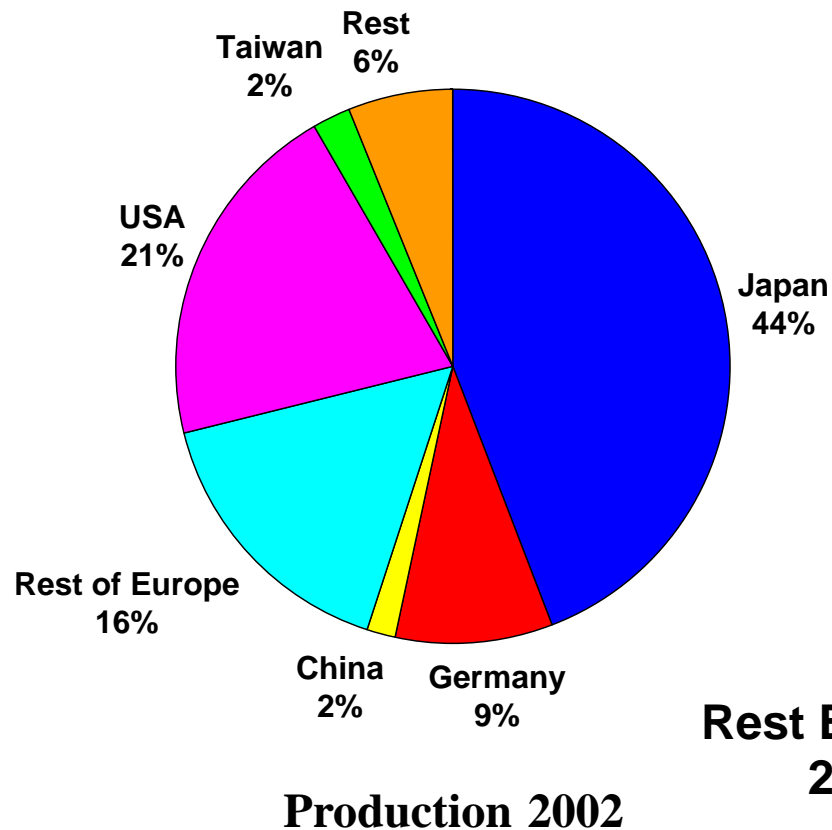


EPIA 2012



Solar cell production around the world

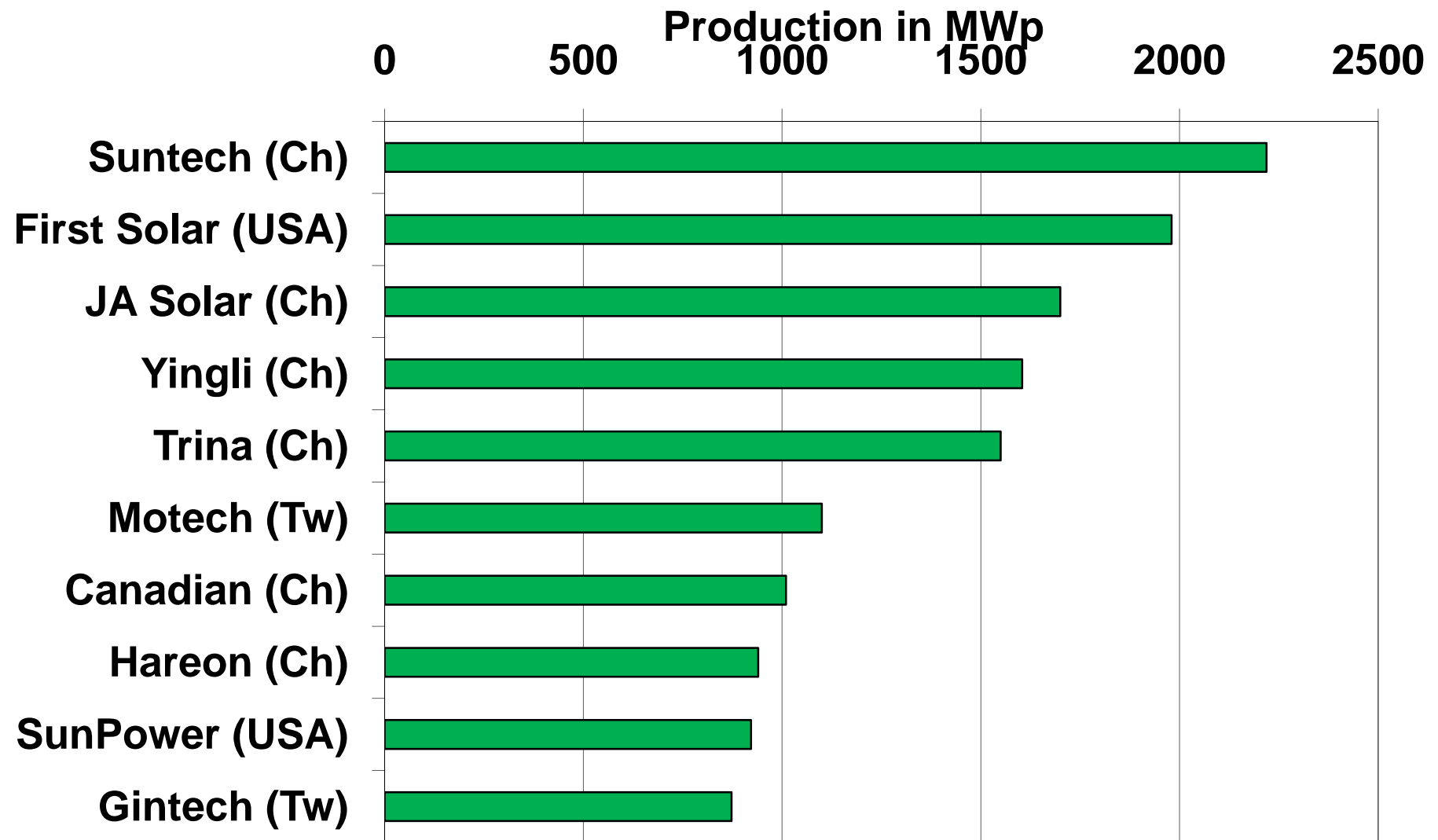
Photon International, 3/2003 & 3/2012



Production has moved recently to asiatic countries, specially China



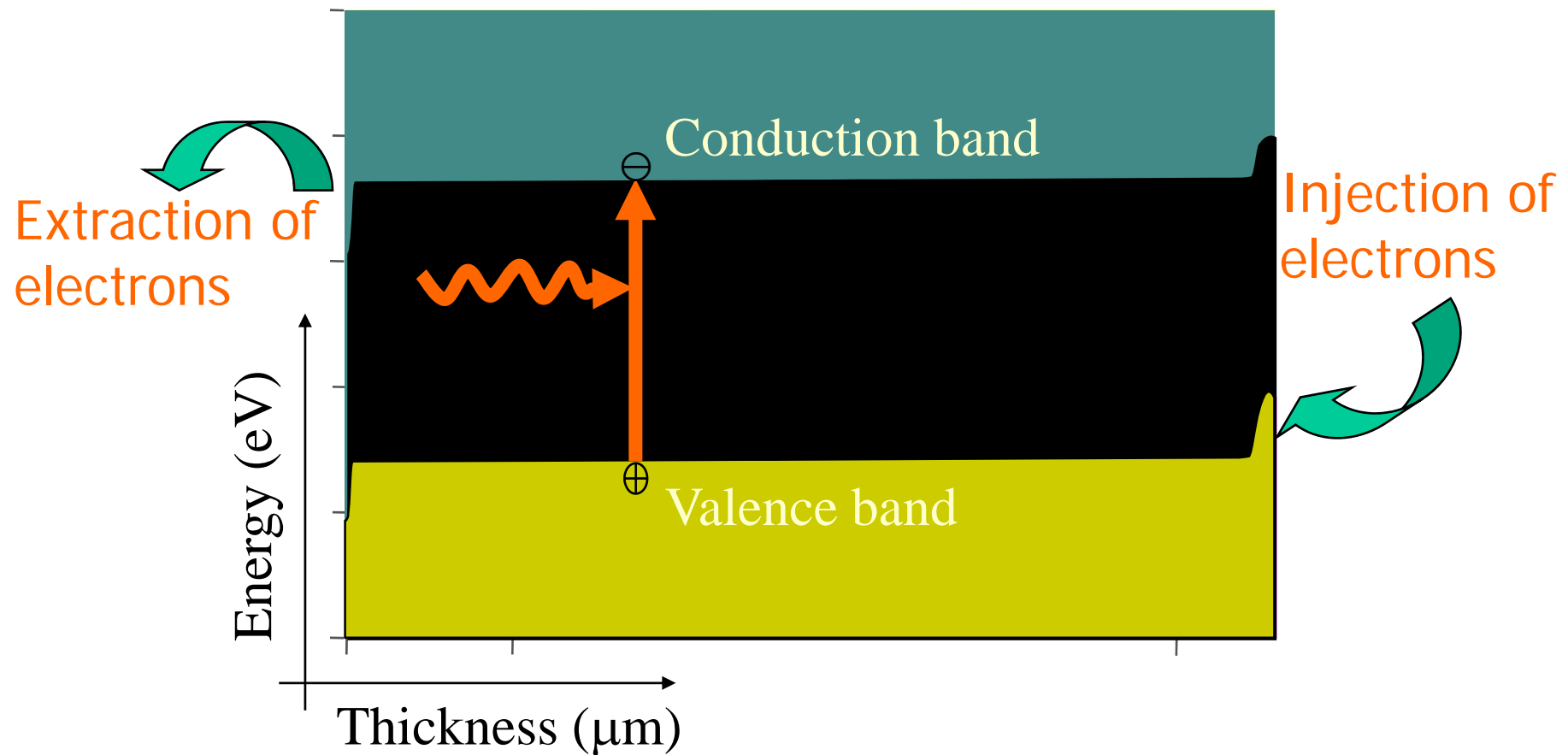
Solar cell production: top ten



Photon International 3/2012



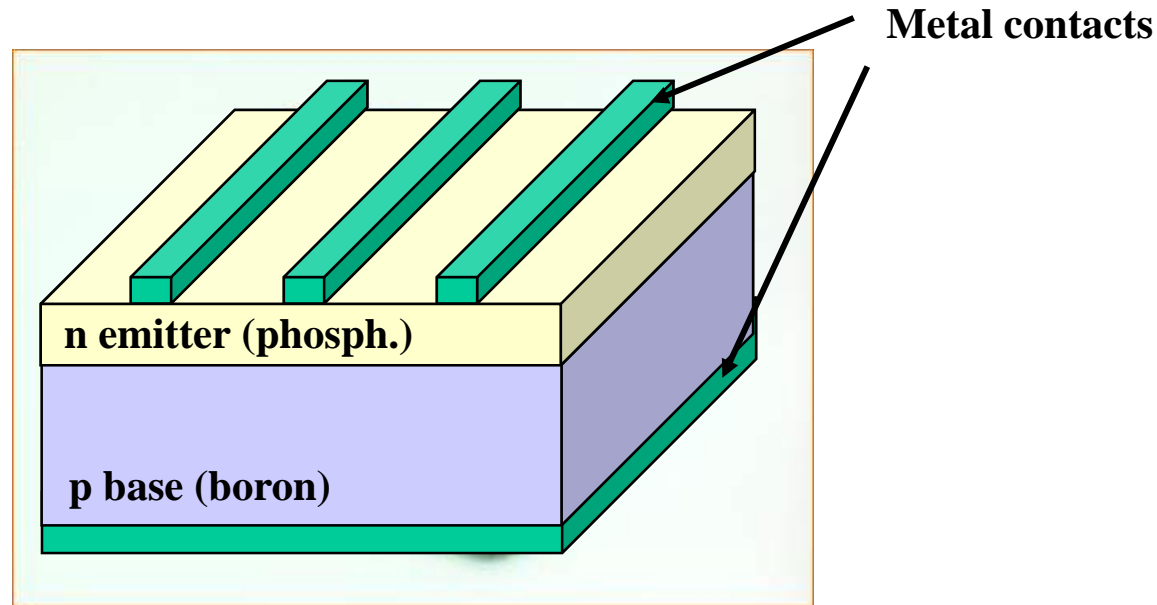
The grounds of the photovoltaic effect



- Photons pump electrons from valence to conduction band
- Appropriate contacts insure conduction band electrons are delivered to load and recovered by the valence band



Design of solar cells



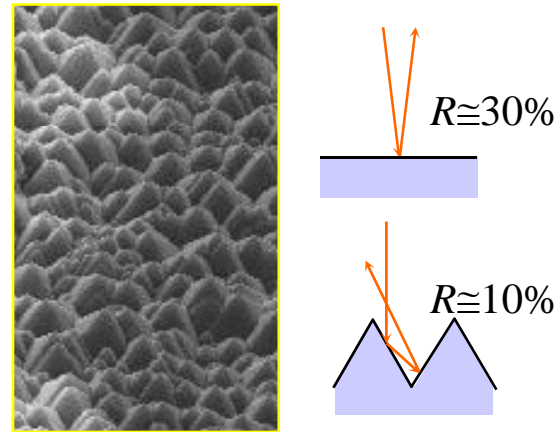
Cell is design to maximise the power delivered:

- **Increasing generation of e^- / h^+ pairs (i.e, absorption of light)**
- **Reducing recombination (in bulk and surface)**
- **Minimising resistive effects**

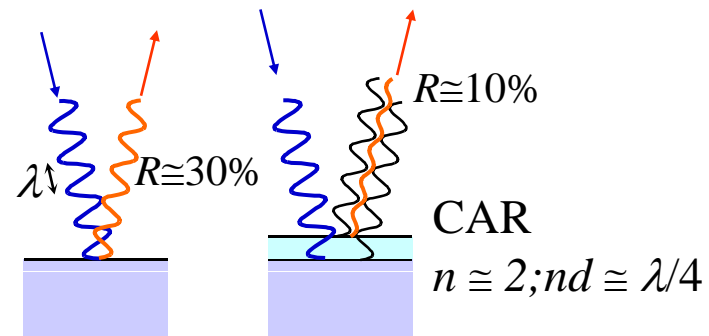


Increasing light absorption

✓ Texturing



✓ Anti-reflection coatings

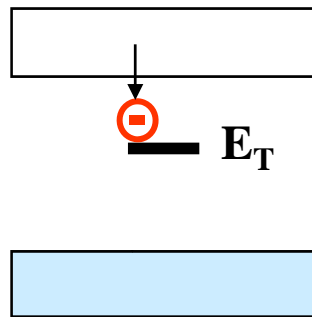


✓ Texturing + anti-reflection coating $\Rightarrow R \approx 1\%$



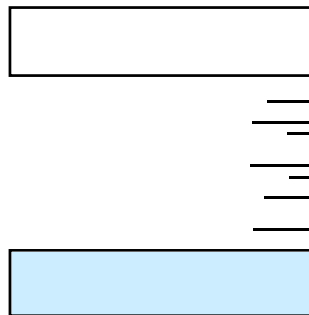
Recombination losses

Crystalline defects and metal impurities produce recombination through traps:



- ✓ Use of high quality materials
- ✓ Fabrication in “clean rooms”
- ✓ Techniques to *getter* contaminant impurities
- ✓ Techniques to reduce recombination action of defects (*hydrogenation*)

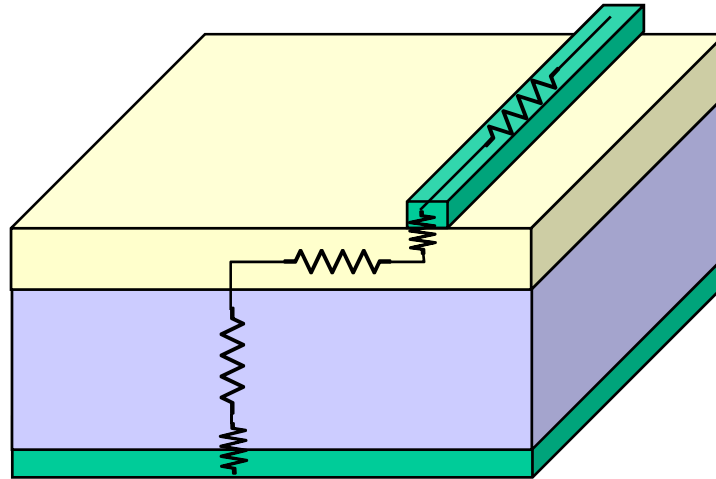
Surface, a radical discontinuity in the crystal:



- ✓ “Passivation” of dangling bonds by deposition of dielectrics
- ✓ Diffusion of a dopant with the same type of the base (Back Surface Field)



Resistive losses

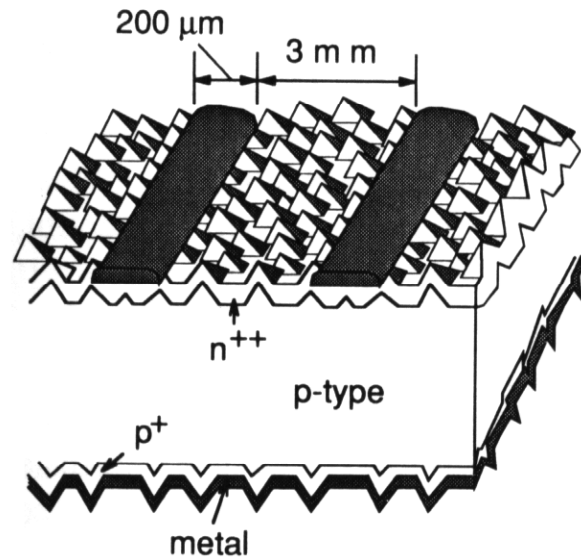


- ✓ Bulk and emitter resistances, subject to compromises (recombination, voltage...)
- ✓ Choice of appropriate metals (good contact, good conductivity)
- ✓ Contact resistance is reduced for highly doped silicon
- ✓ Design of metal grid subject to compromises (shadow, minimum finger width...)



Solar cell structures

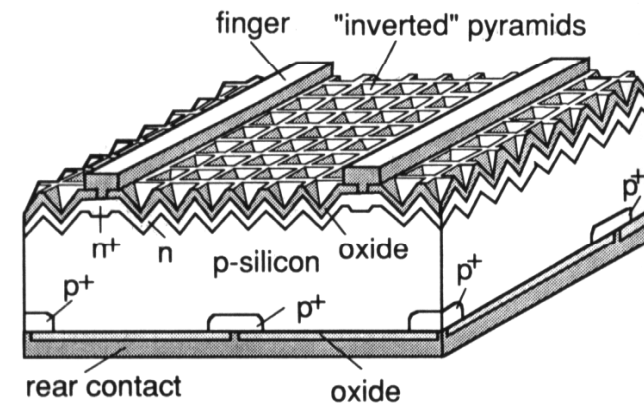
Industrial



- “Solar grade” substrates
- Reduced number of process steps
- Screenprinted metallisation

EFFICIENCY \approx 16-18%

Laboratory



- High quality substrate (Float Zone)
- Processed at “cleanroom”
- Photolithography and metallisation by evaporation in vacuum

EFFICIENCY \approx 25%

$$\text{Efficiency} = \frac{\text{Electrical Power delivered}}{\text{Luminous Power received}}$$



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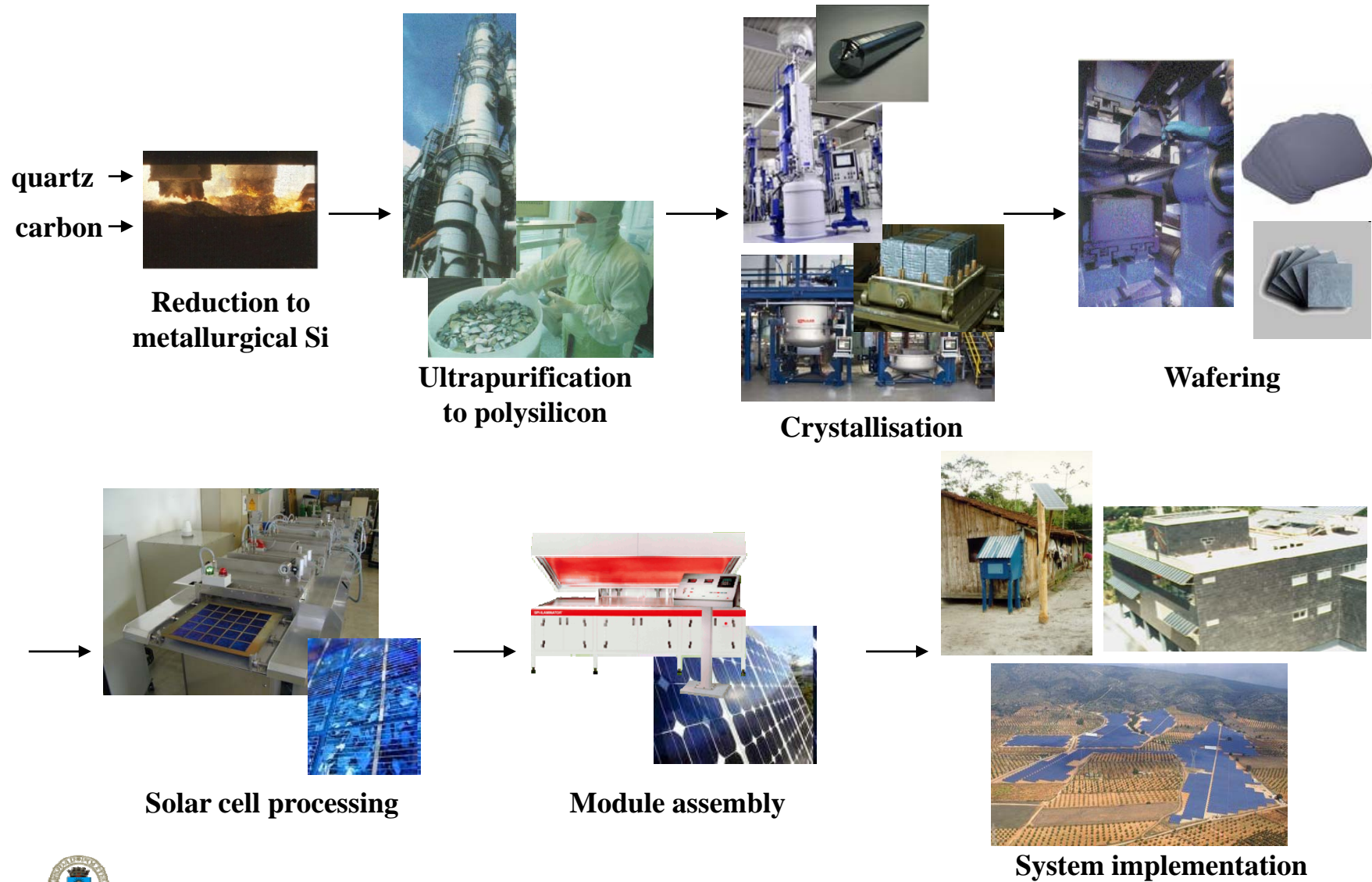
Economical and environmental issues

Alternatives to cristalline silicon technology

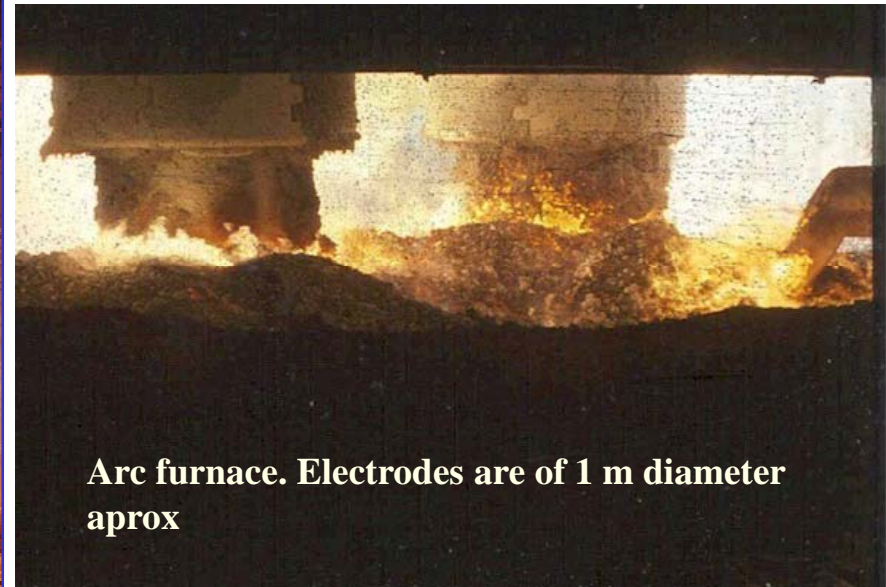
Conclusions



The Si value chain: from silica to systems



Metallurgical silicon production

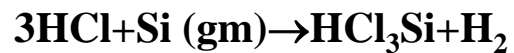
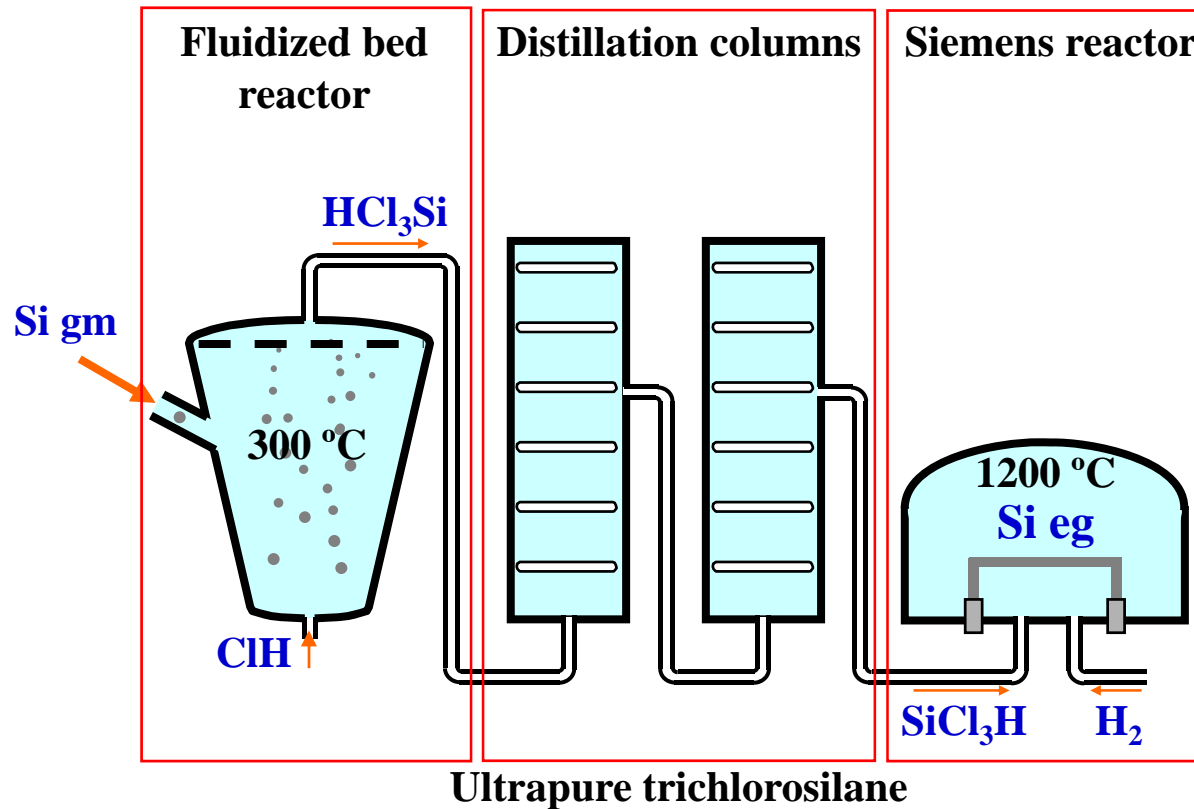


Arc furnace. Electrodes are of 1 m diameter aprox

- Reduction of quartz with carbon in an arc furnace $\text{SiO}_2 + 2\text{C} \rightarrow \text{Si} + 2\text{CO}$
- Product: metallurgical silicon, 99% pure
- Electronics and Photovoltaics only use a small fraction (~10-15%) of the total production, which is devoted mainly to metallurgical industry

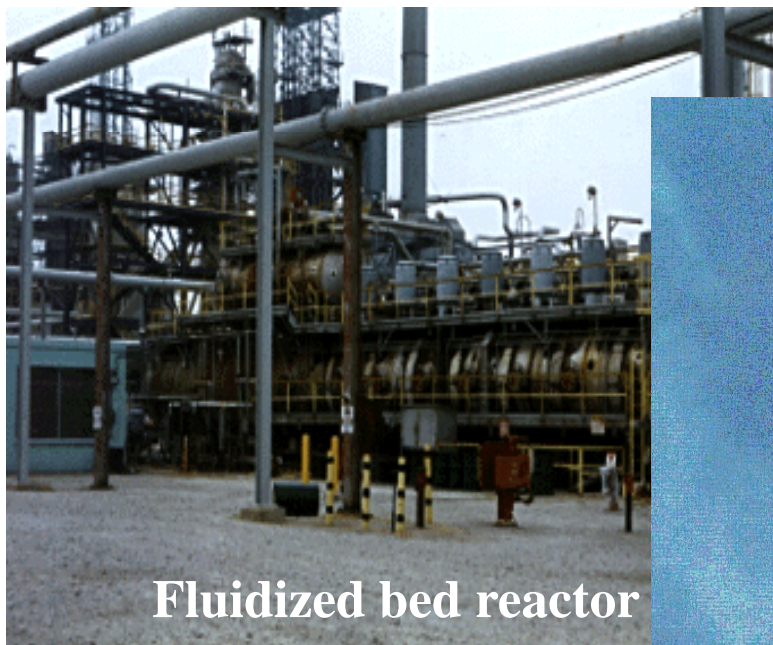


Electronic grade silicon production (polysilicon)



- **Raw materials:** metallurgical silicon and HCl
- **Product:** Electronic grade silicon (99.9999999% pure)
- **High energy consumption** (100 kWh/kg)





Fluidized bed reactor



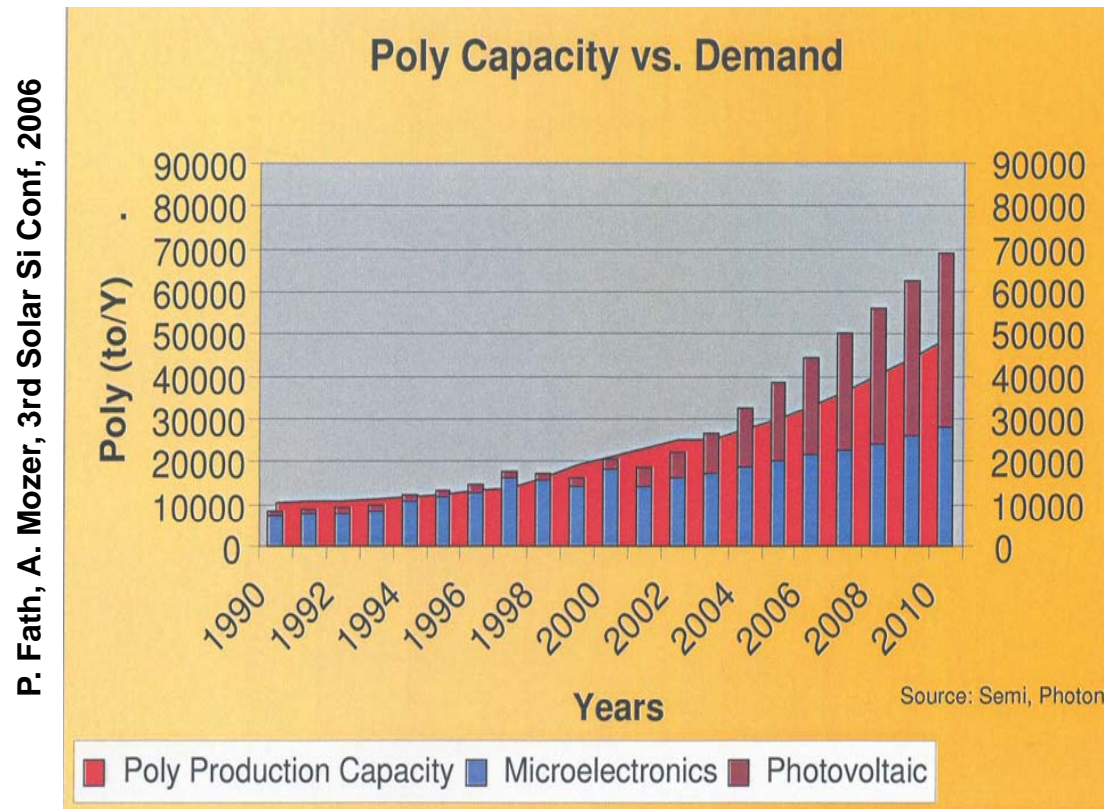
Distillation column



Polysilicon



The revolution in the Si feedstock market

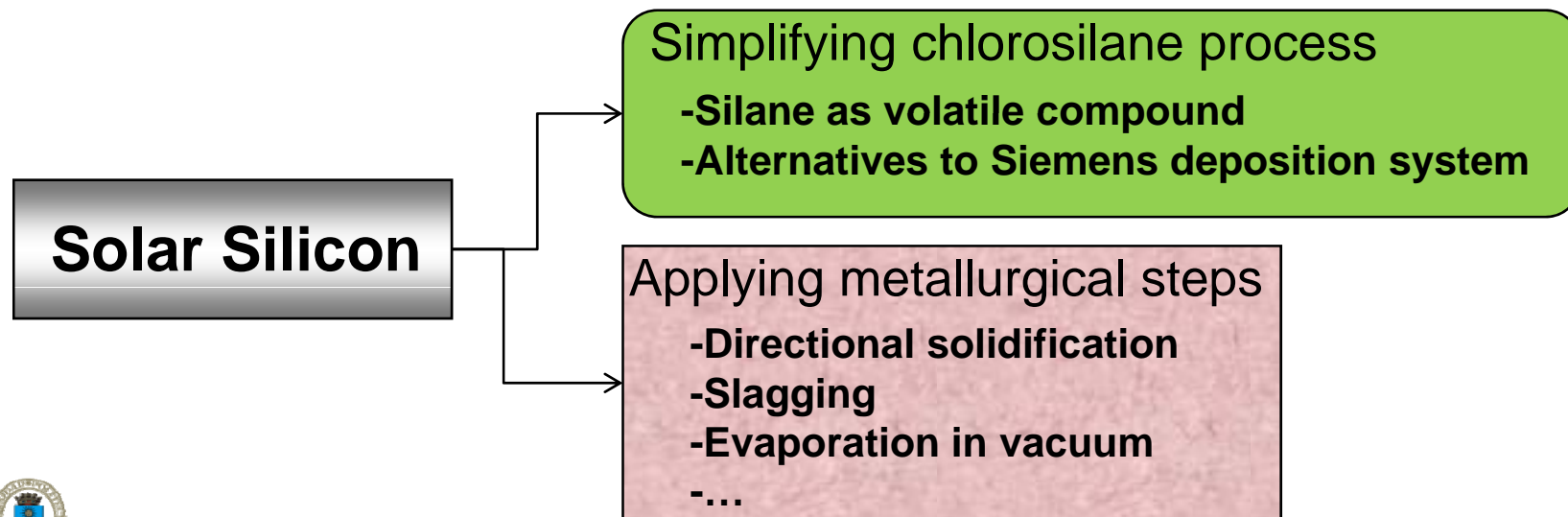


- **2006-2007: Silicon shortage**
 - **Slow reaction of polysilicon producers to expand capacity**
 - **New entrants trying to acquire the technology**
 - **Intensive R&D on polysilicon purification for solar**

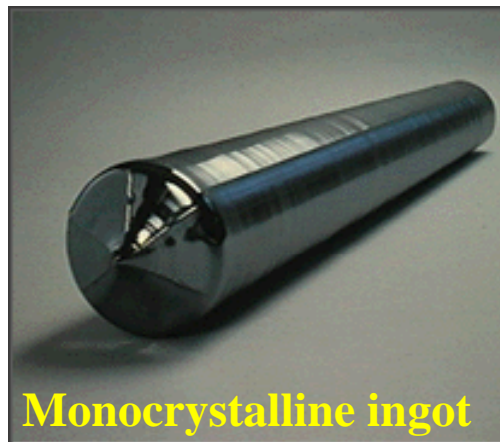
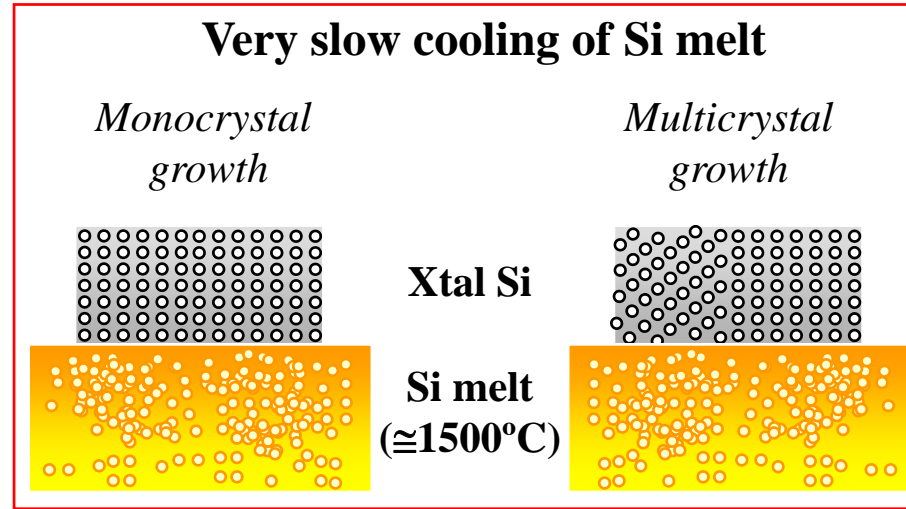
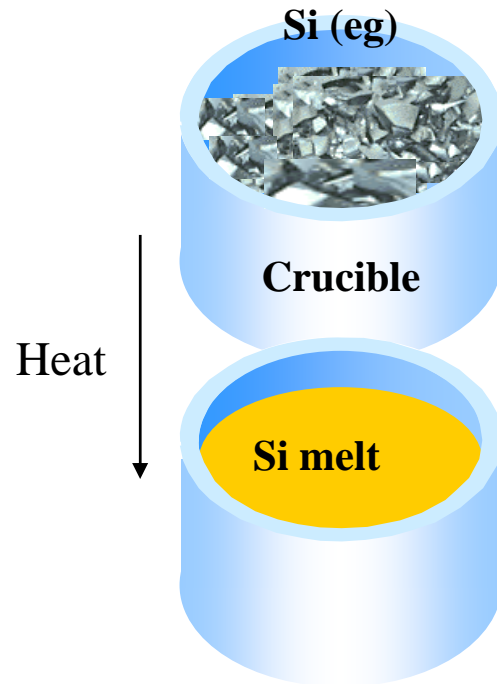


The revolution in the Si feedstock market (2)

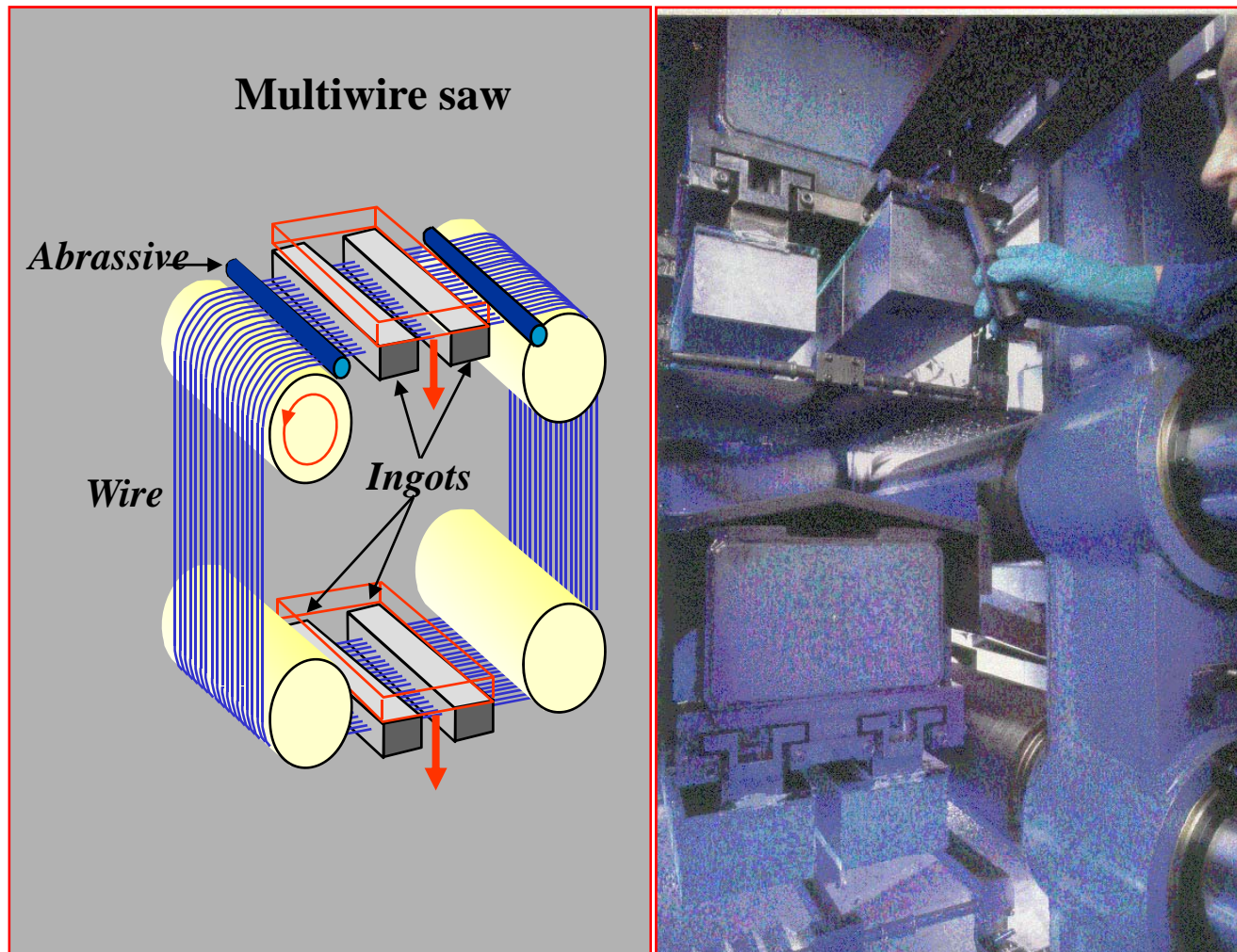
- **2009: Quick change of scenario towards oversupply**
 - New capacity installed in a context of market slow-down
 - Pressure on new entrants, that need to demonstrate good material quality and low costs
- **In the medium and long run:**
 - PolySi production structure changing to a “distributed” one: ~5000 t of Si for a 1000 MWp PV plant
 - Technology changes when targeting “Solar Silicon”



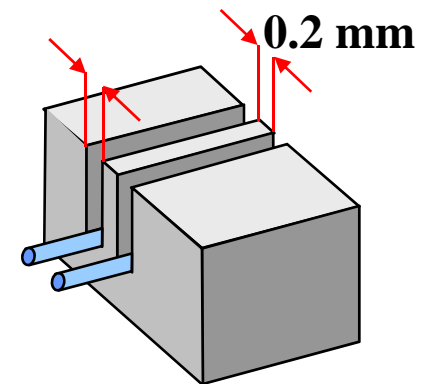
Crystallisation



Wafering

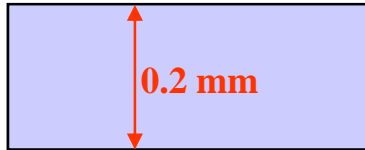


**Kerf losses of
around 50%**



Solar cell processing (I)

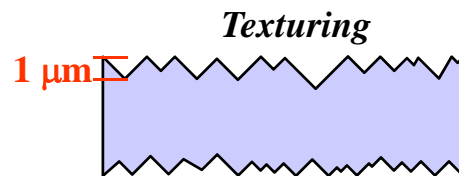
Wafers 156 cm² p-type (B 10¹⁶ cm⁻³)



Cleaning and etching

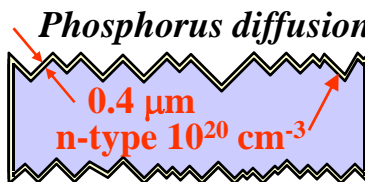


KOH



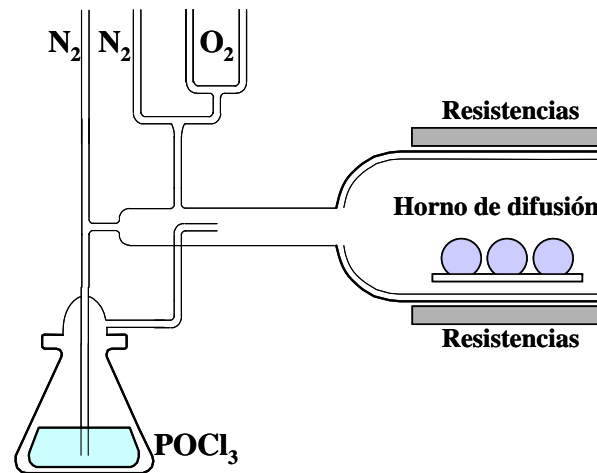
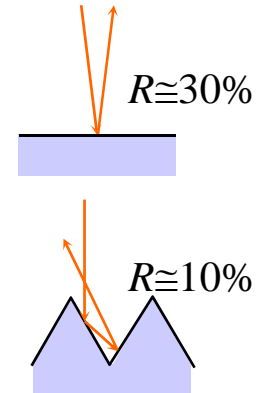
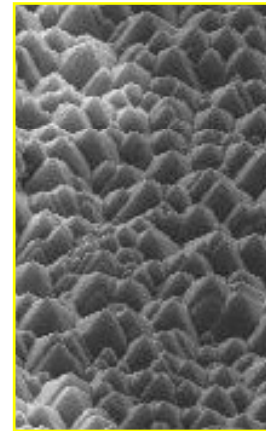
KOH bath
85 °C

Cleaning



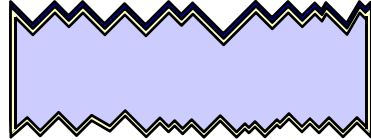
900 °C, 1/2 h

Cleaning



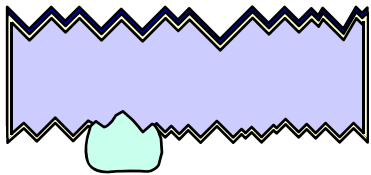
Solar cell processing (II)

Anti-reflecting coating



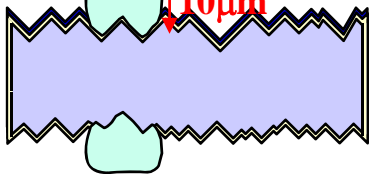
CVD
 $\text{SiN}_x\text{:H}$

Rear contact

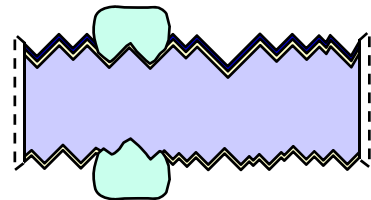


Screenprinting of
a conductive paste
Fired in a IR furnace
(800°C, 10 min)

Front contact
100 μm
10 μm



Junction isolation

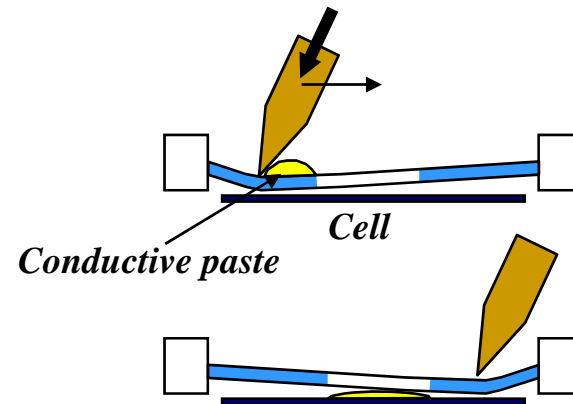
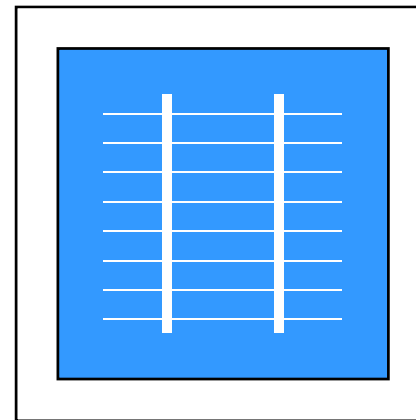
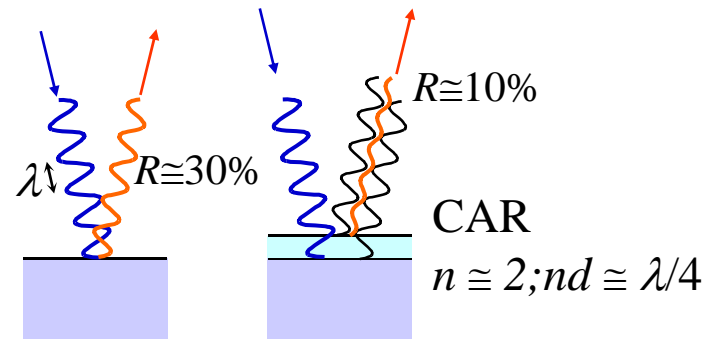


Plasma
etch

Measure and classify

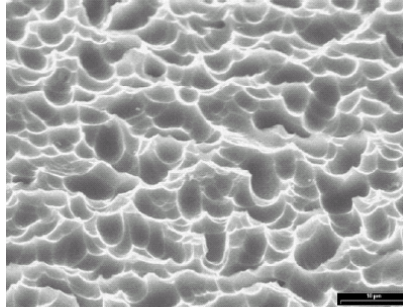


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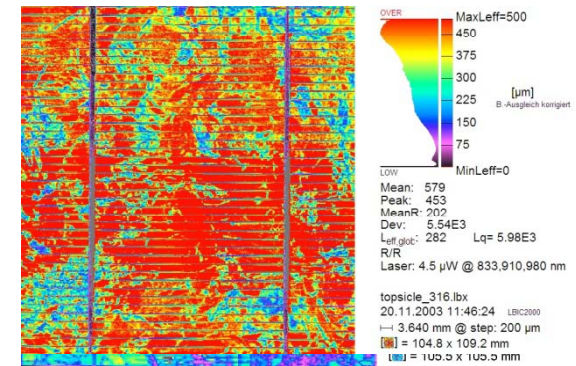


Solar cell processing for multicrystalline Si

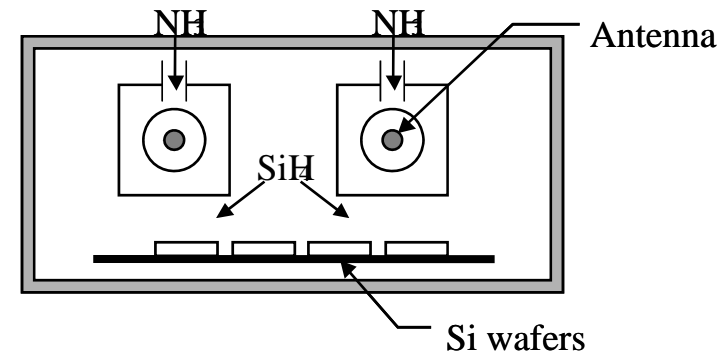
Alternatives to alkaline texturing:
acidic texturing, RIE,...

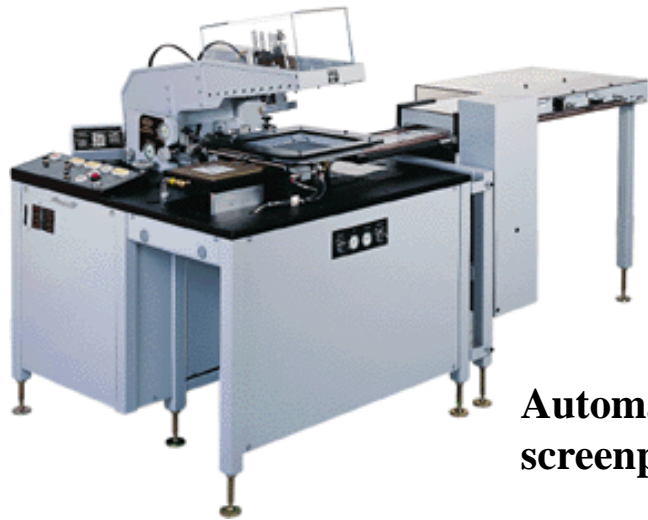


Integration of tailored *gettering* processes

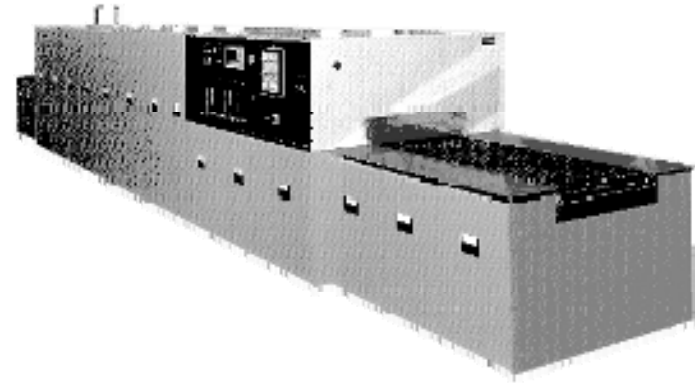


Hydrogenation by Plasma-Enhanced Chemical Vapor
Deposition during the SiN_x AR coating





**Automatic
screenprinter**



**In-line furnace for
firing**



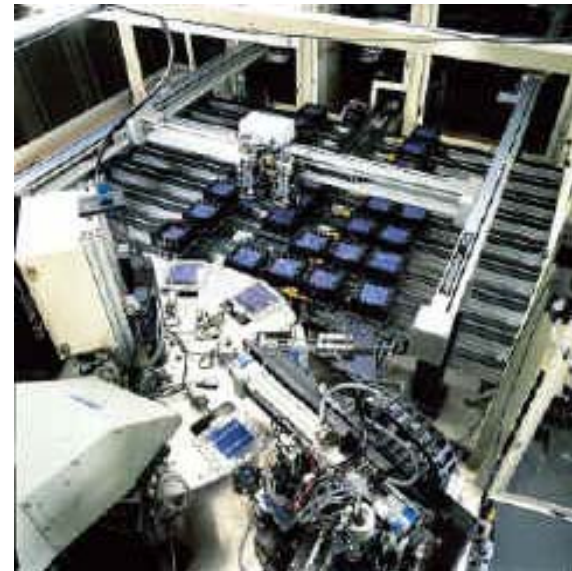
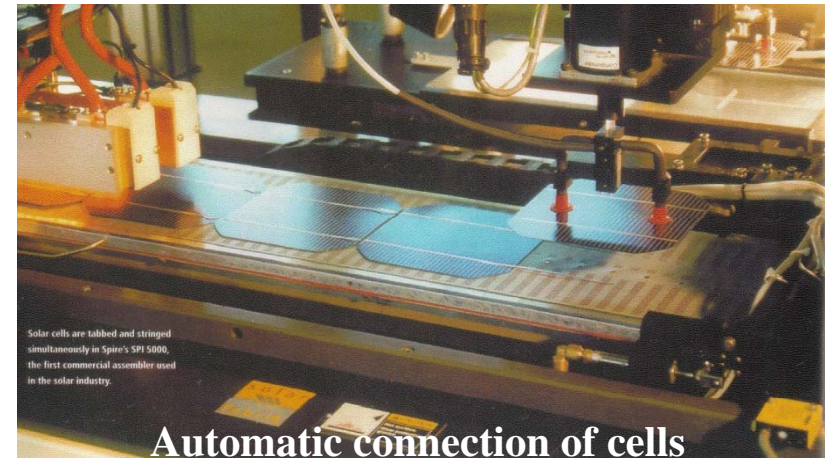
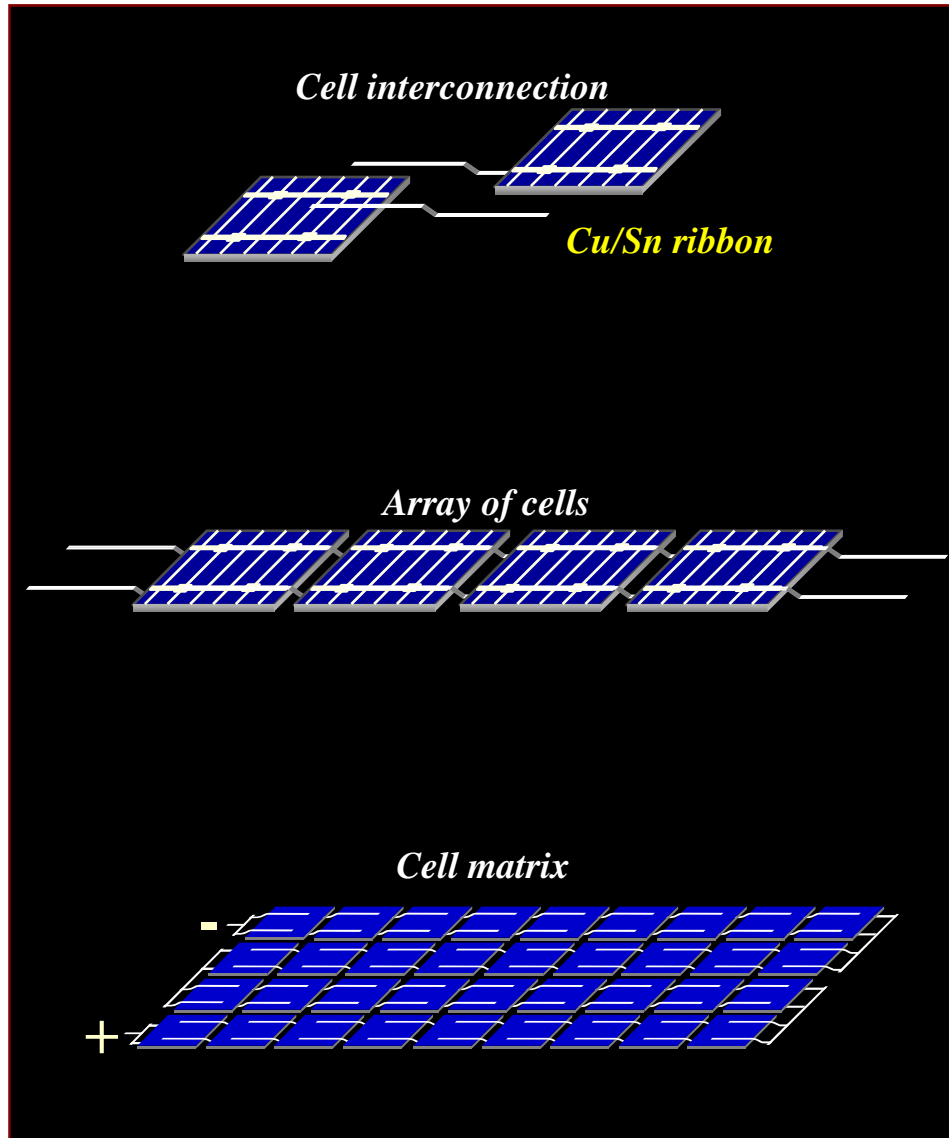
A solar cell factory



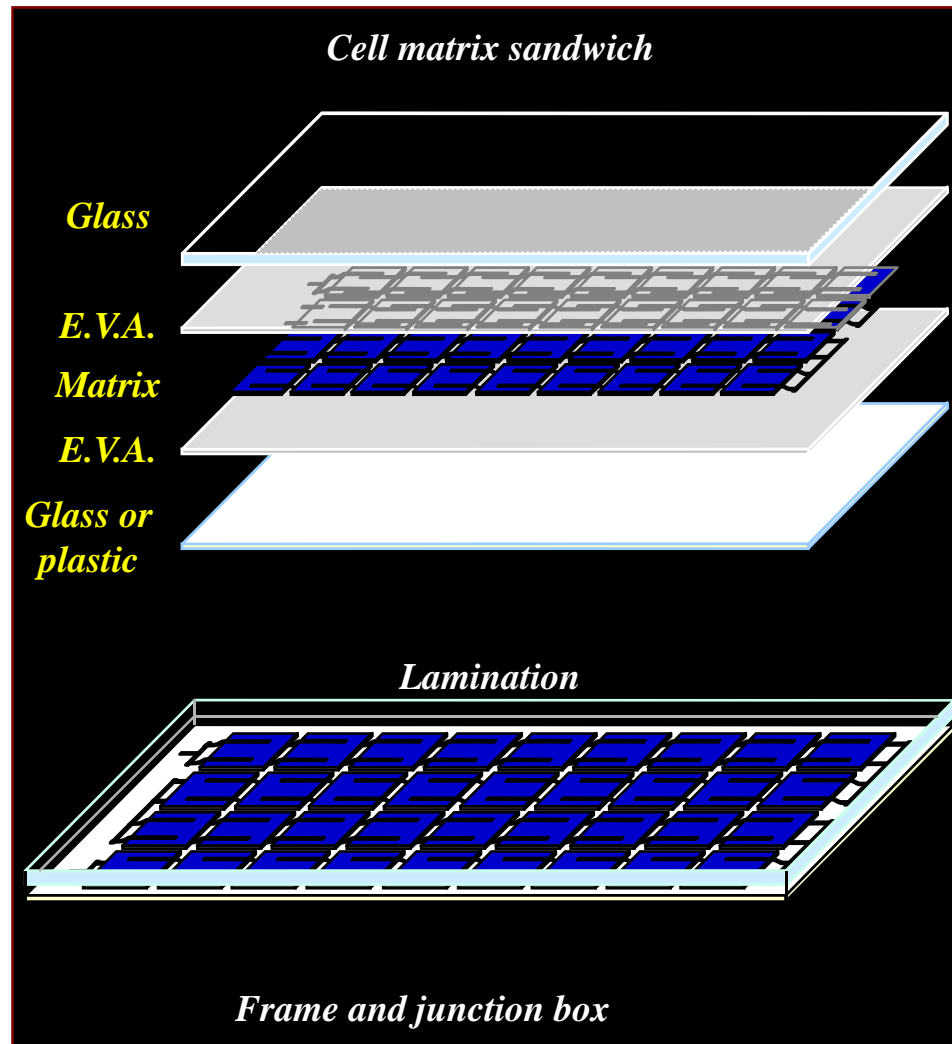
Diffusion furnaces



Module assembly (I)



Module assembly (II)



Lamination:

Pressure at 100°C + Curing at 150°C:

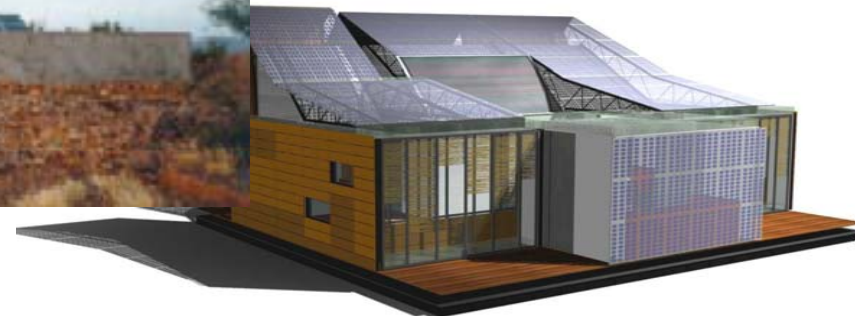
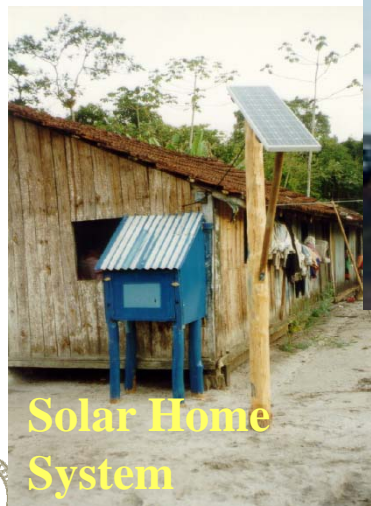
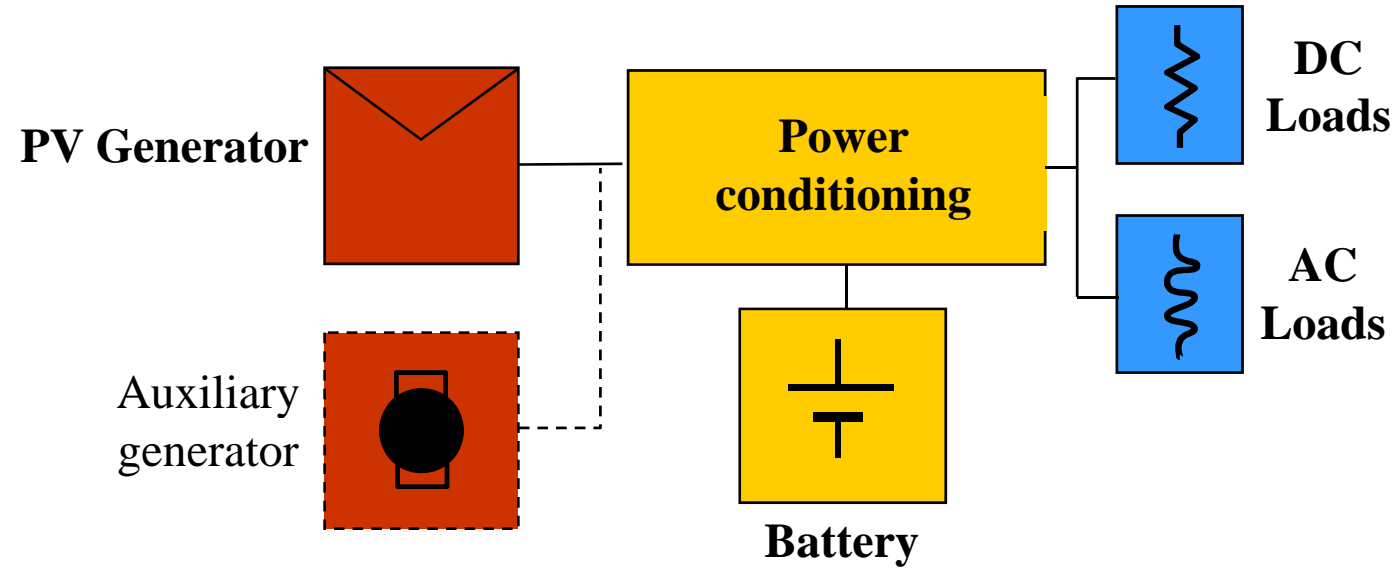
Cells are soaked in the flowing EVA, which becomes transparent and solidifies



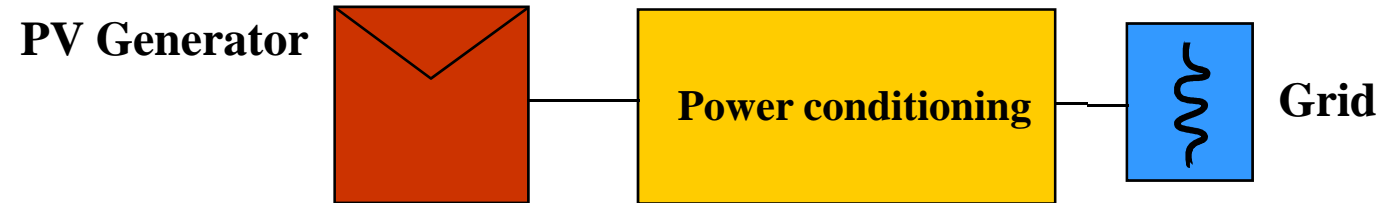
Laminator



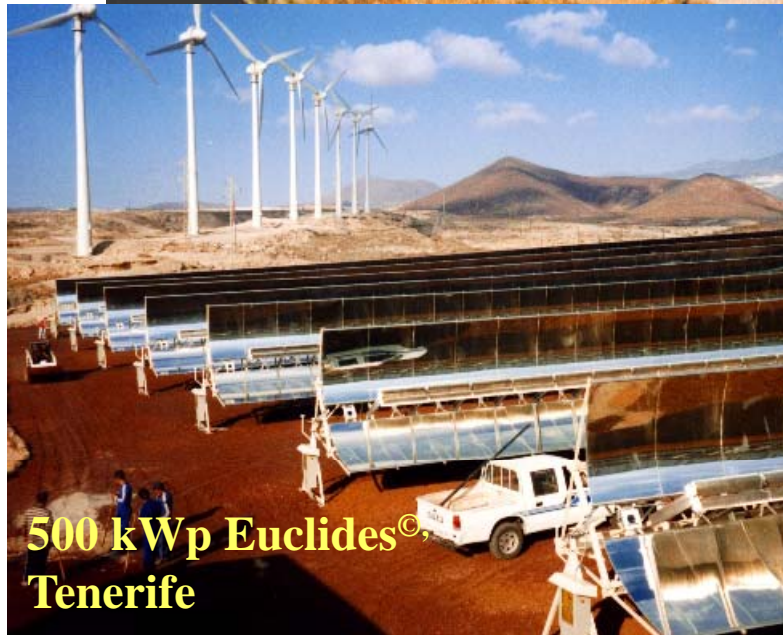
Off-grid installations



Grid connected instalations



Solar farms



BoS equipments

Batteries



Regulator

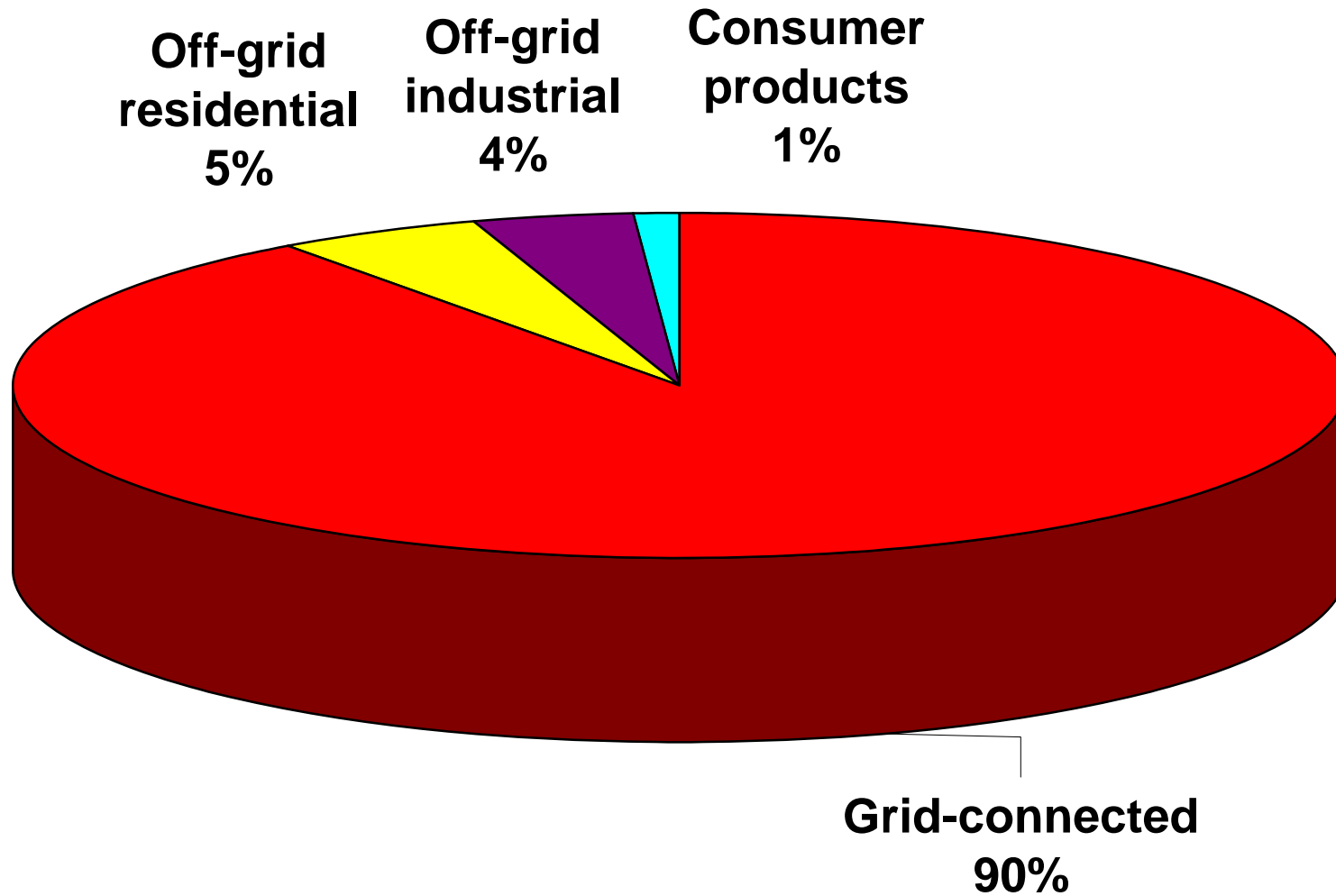


DC/AC
converters



PV market breakdown (2007)

EPIA/Greenpeace, 2008



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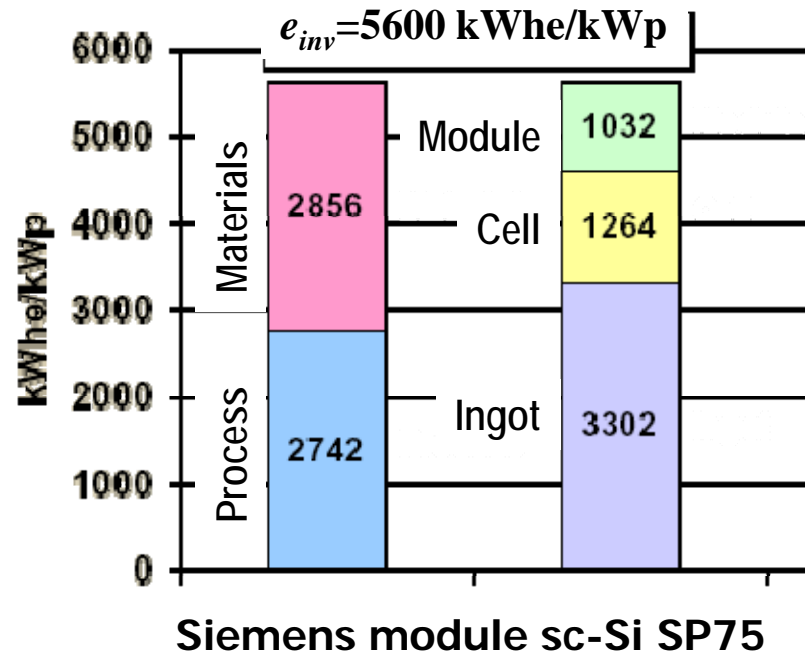
Conclusions



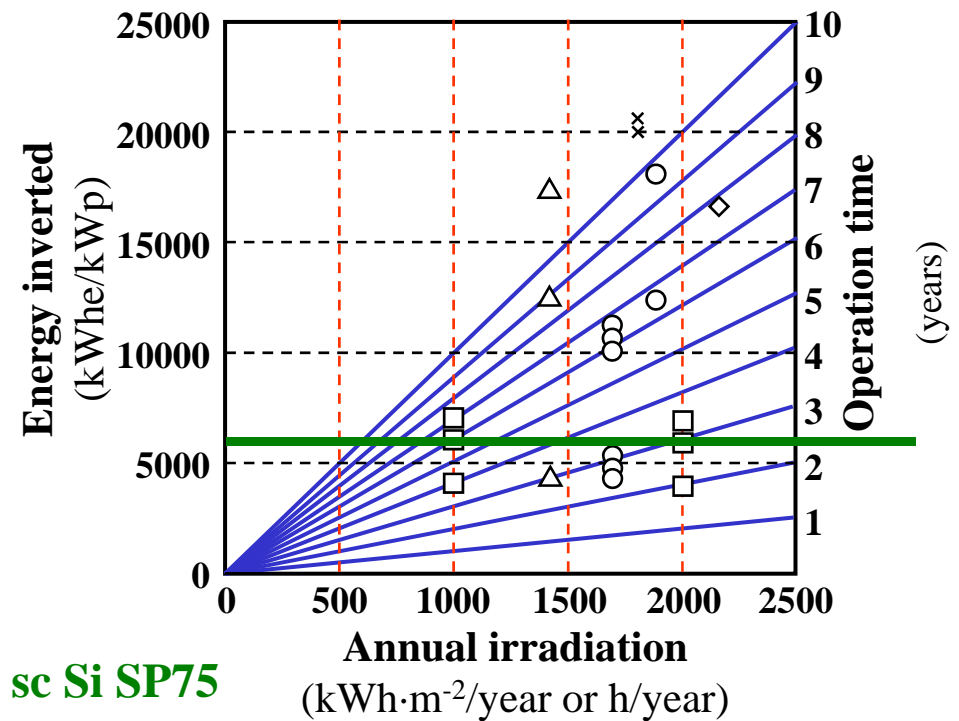
Energy Pay Back Time (EPBT)

Time needed by the PV system to give back the energy invested in its fabrication

$$EPBT = \frac{\text{Invested energy}}{\text{Energy produced in a year}}$$



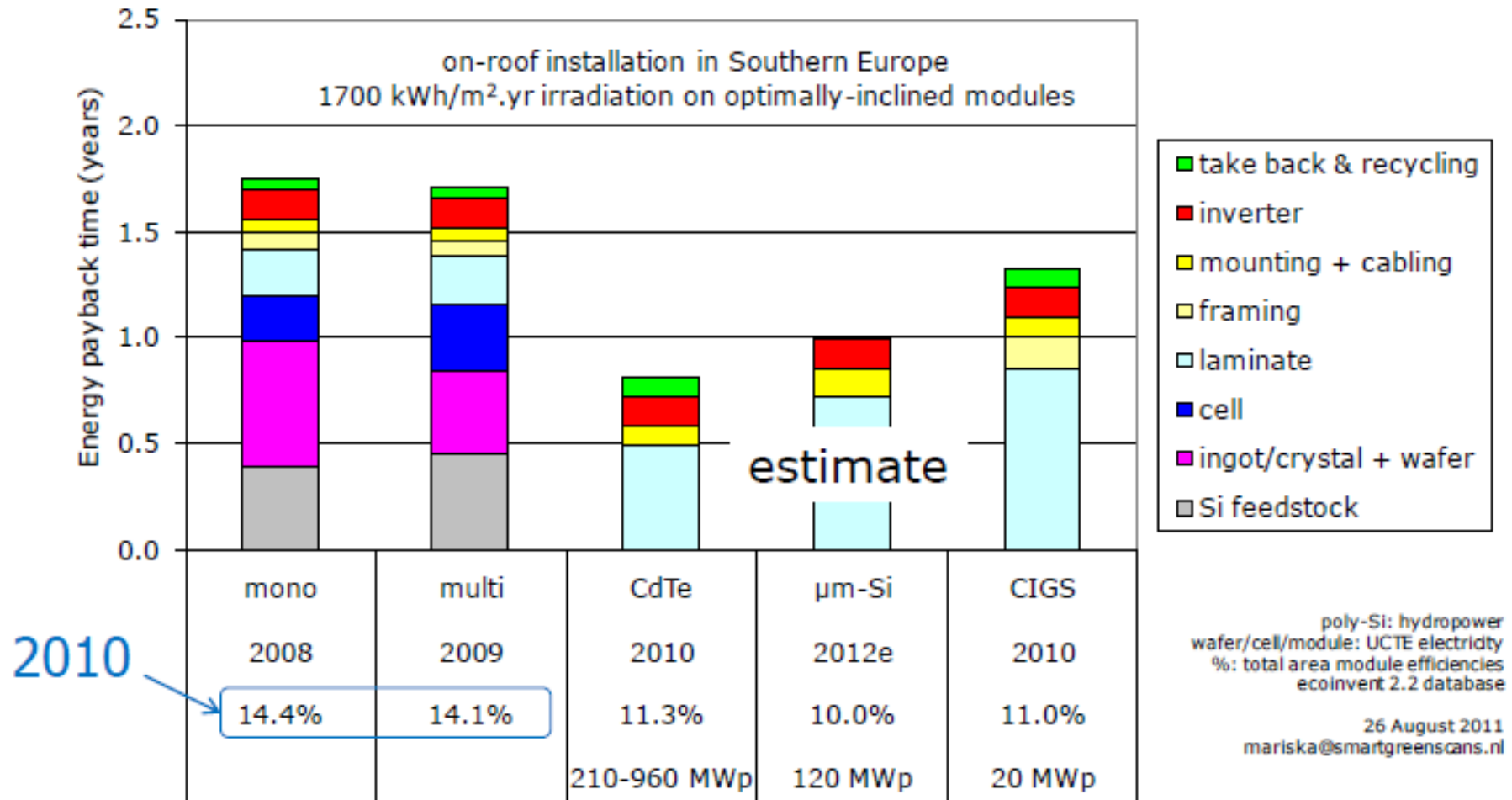
K. Knapp y T. Jesters, "PV payback".
Home Power, **80** (2001)



Siemens module sc Si SP75
(Knapp & Jester 2000)



Energy Pay Back Time (II)

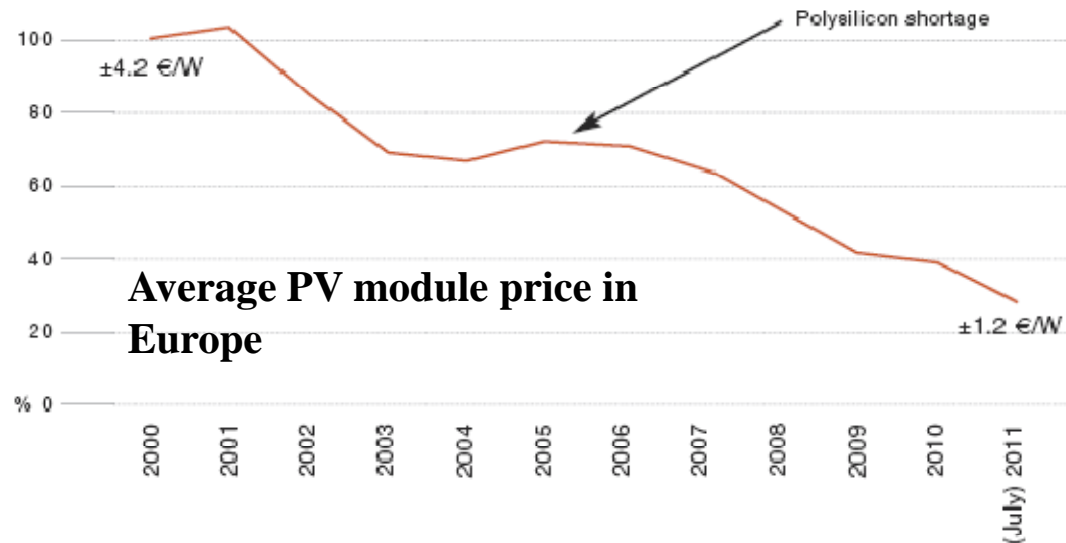


Source: de Wild-Scholten, 26th European PVSEC, 2011

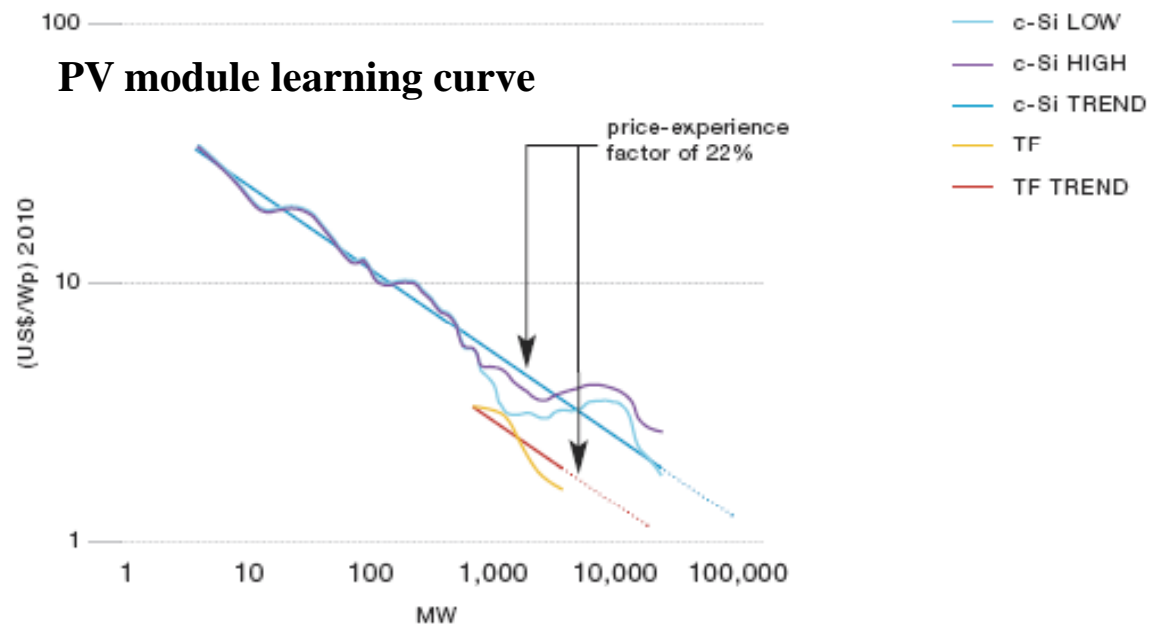


Evolution of technology price

EPIA 2011



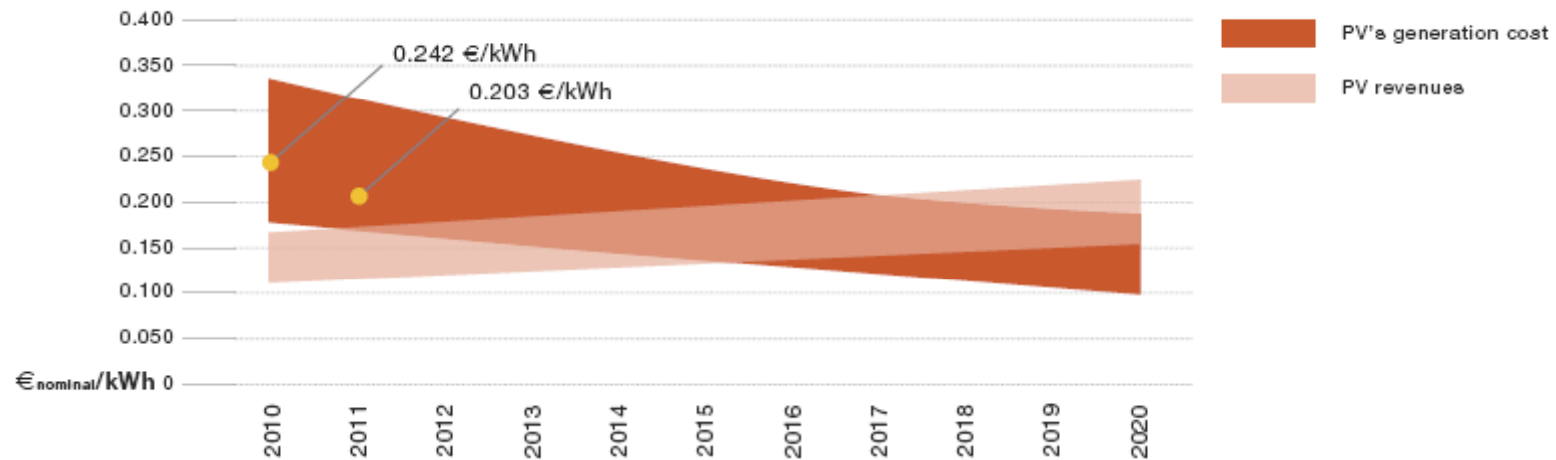
Steady reduction of PV price, closing the gap to competitiveness



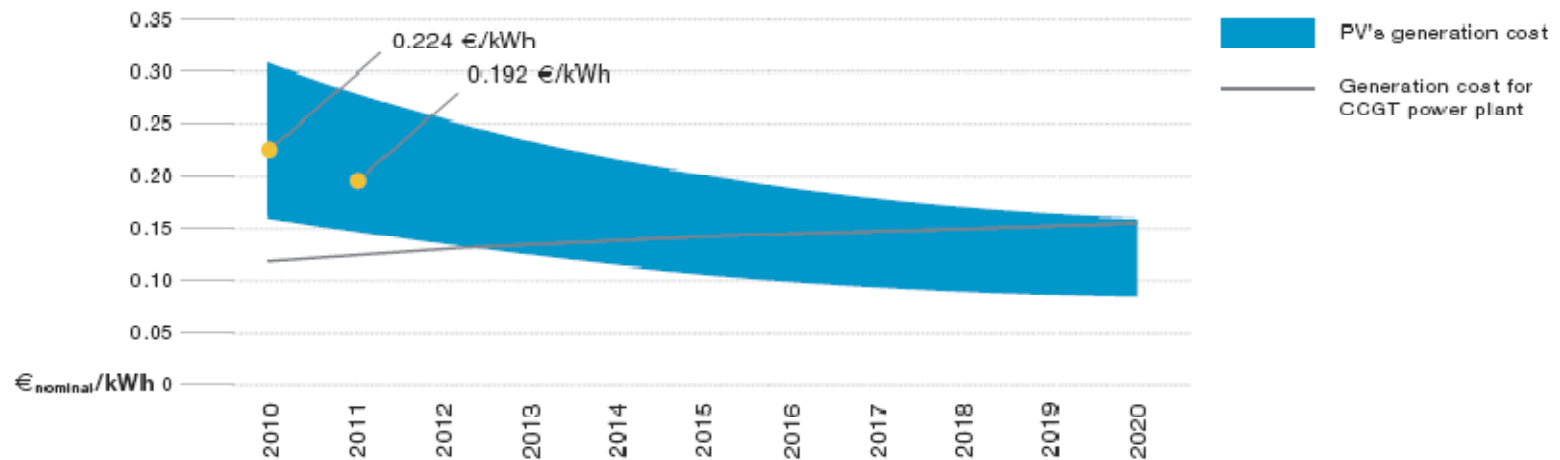
Evolution of technology price (2)

EPIA 2011

PV commercial system (100 kW)



Large ground-mounted PV system (range of MW)

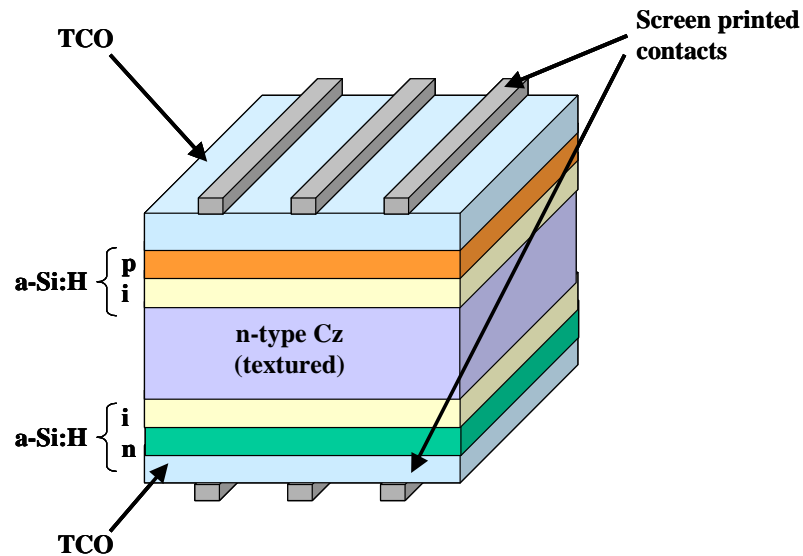


Most major EU markets can reach competitiveness before 2020 under a mature market assumption



Advanced crystalline silicon technologies

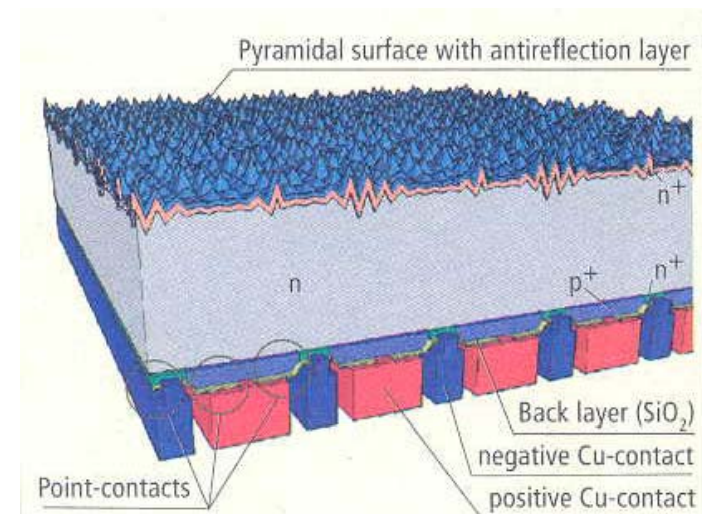
HIT: Heterojunction with Intrinsic Thin layer (Sanyo)



- n-type wafers
- a-Si layers deposited at 200°C.
- Efficiencies at industrial level > 20%
- Bifacial structure

- Both p+ and n+ contacts at the rear, with two alternated “comb-like” structures
- Use of highest quality wafers
- Efficiencies at industrial level > 22%

Point-Contact Cell (SunPower)



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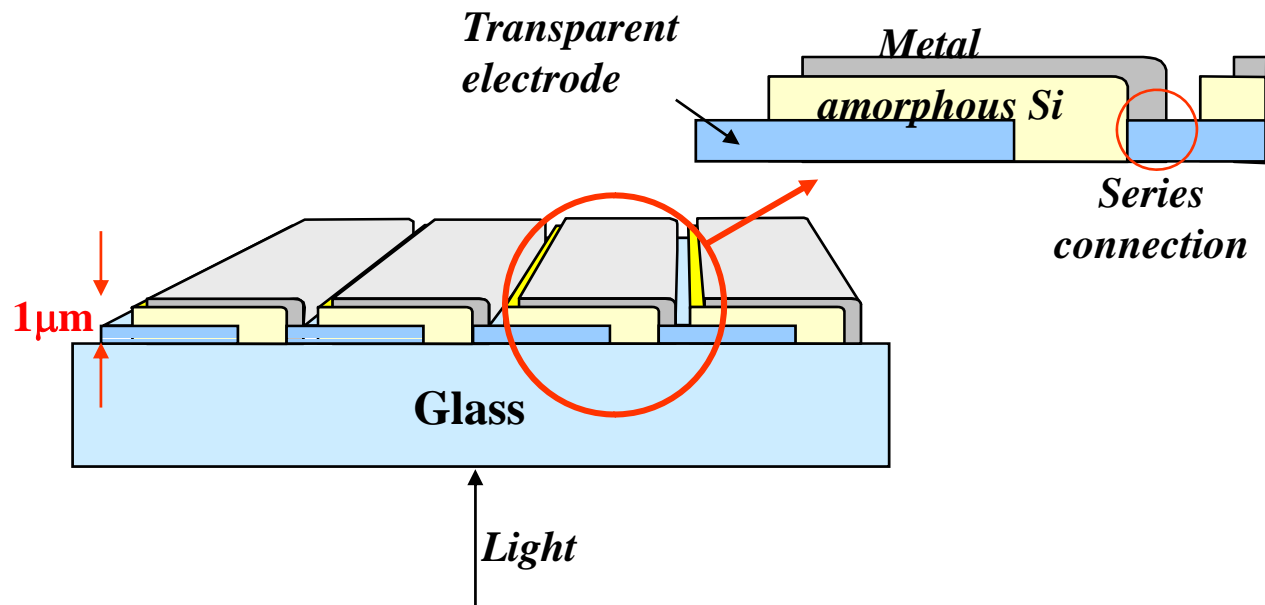
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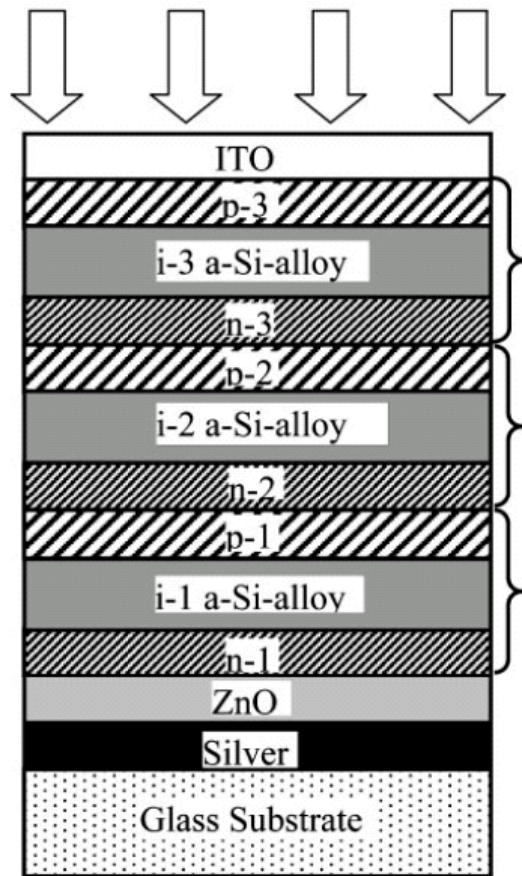
Low cost approaches: Thin films

A big portion of light is absorbed in the first few microns (depending on the material): possibility to SAVE material

Direct fabrication of the module: layers are deposited and interconnected on the substrate (the glass, for instance)



Thin films: a-Si:H



a-Si:H

$E_G \cong 1.3-1.8 \text{ eV}$

Commercial module

$\eta \cong 7 \%$

Laboratory cell

$\eta \cong 12.1 \%$

Advantage

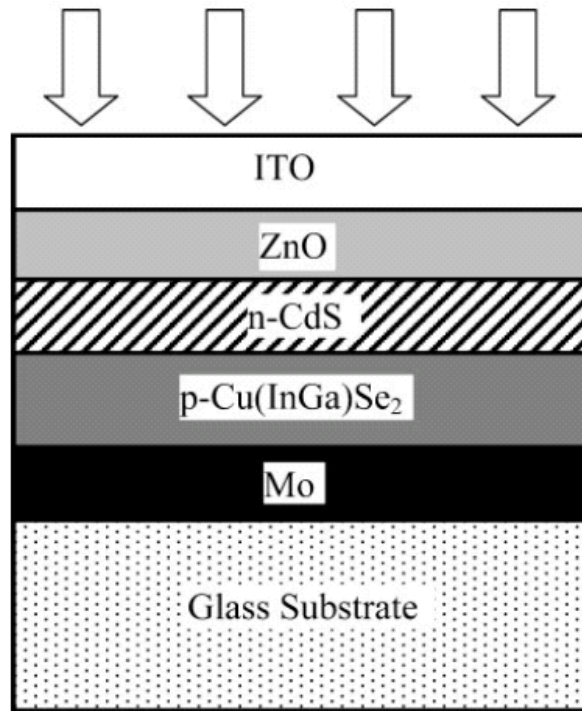
Cost, maturity

Disadvantage

Low estabilised efficiency



Thin films: CIS y CdTe



CIS

$$E_G \cong 1.1\text{eV}$$

Commercial module

$$\eta \cong 12 \%$$

Laboratory cell

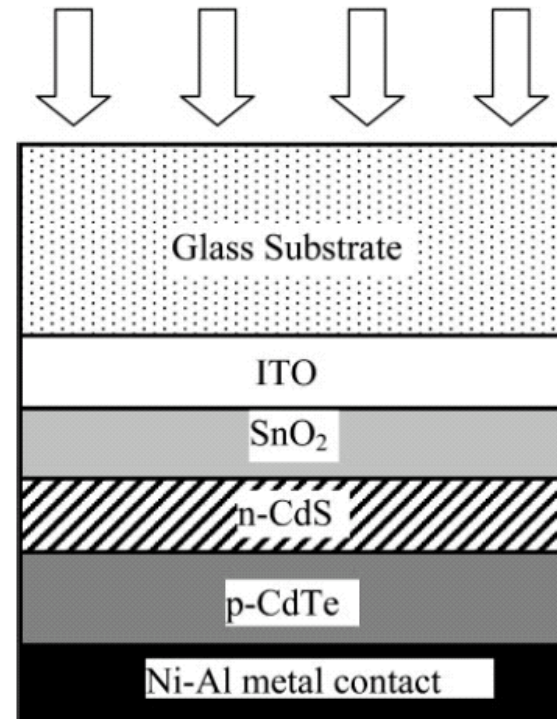
$$\eta \cong 20 \%$$

Advantage

Higher efficiency

Disadvantage

Complex, scarcity of In



CdTe

$$E_G \cong 1.5\text{eV}$$

$$\eta \cong 10 \%$$

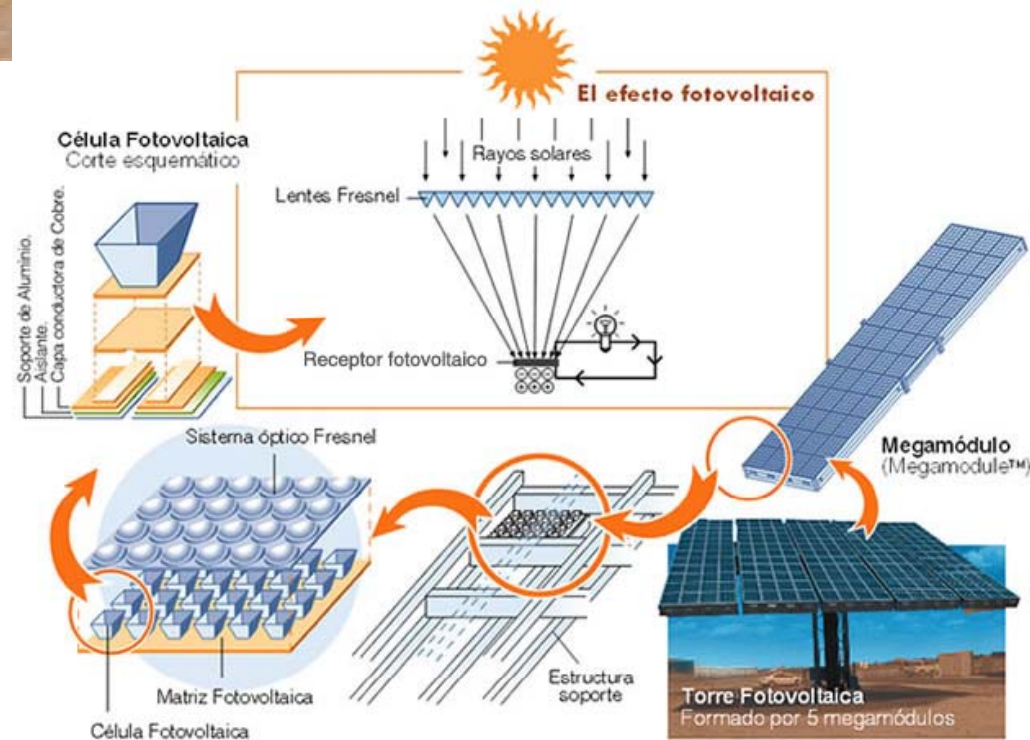
$$\eta \cong 17 \%$$

Lower cost

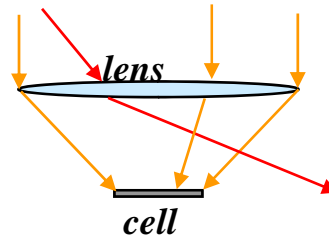
Scarcity and toxicity of Cd



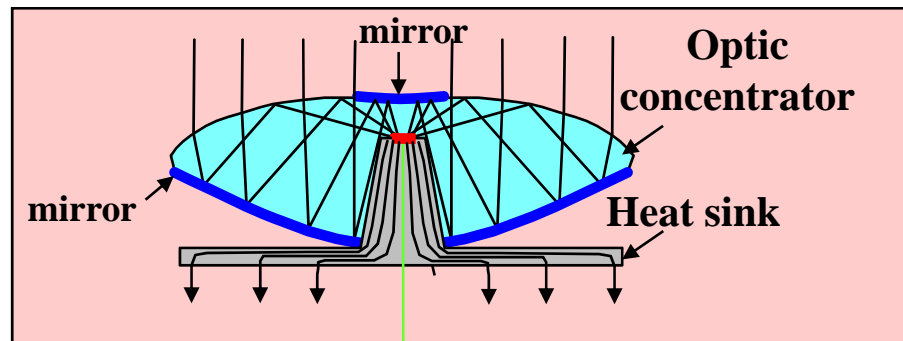
High efficiency approach: PV concentration



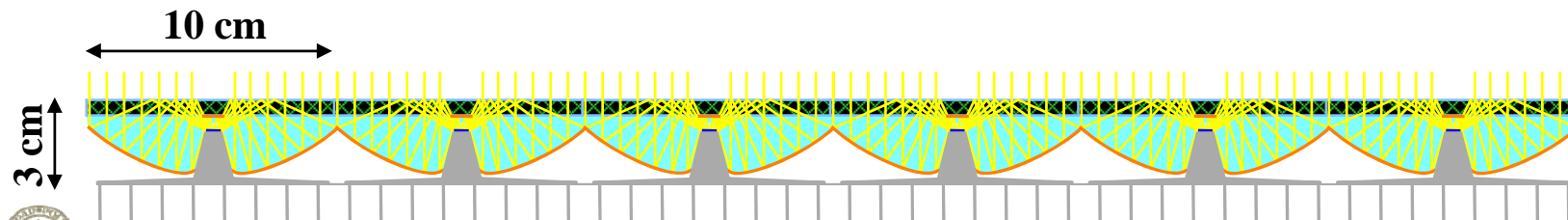
High eff. approach: high concentration with GaAs cells



1.000 suns = 1 MW/m²

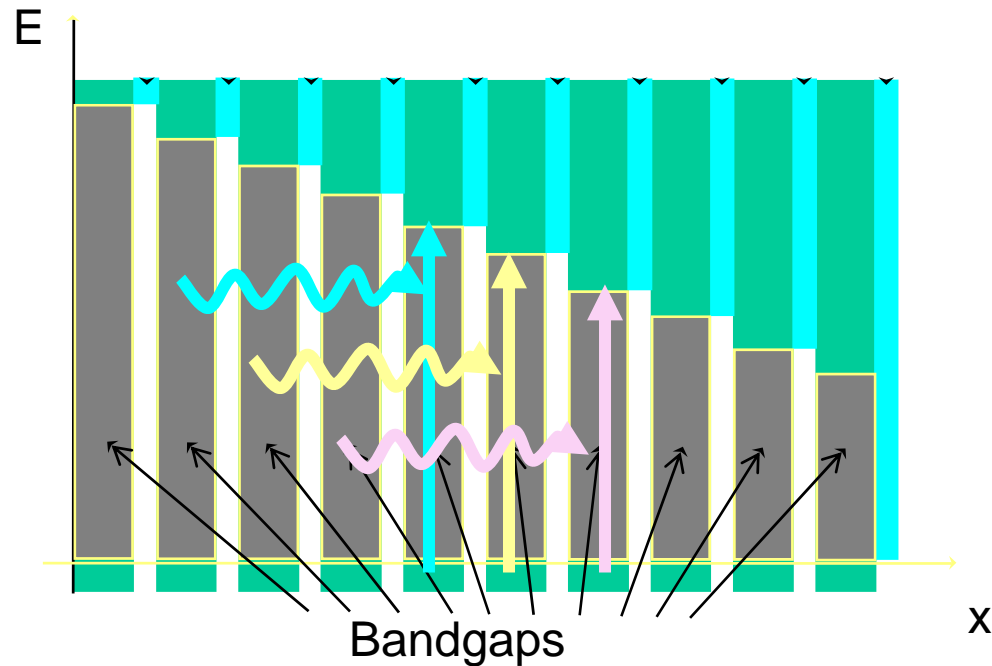


AsGa cell
~30-40% at high
irradiation levels

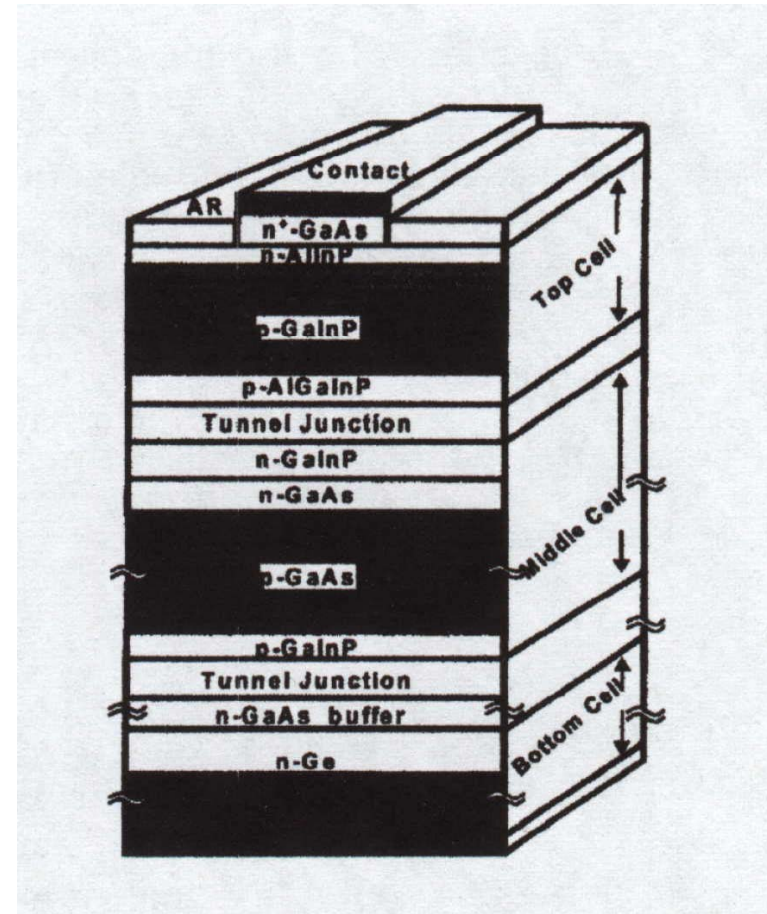


High efficiency approach: Third generation solar cells

- Tandem cell



Theoretical limit: $\eta=86,3\%$

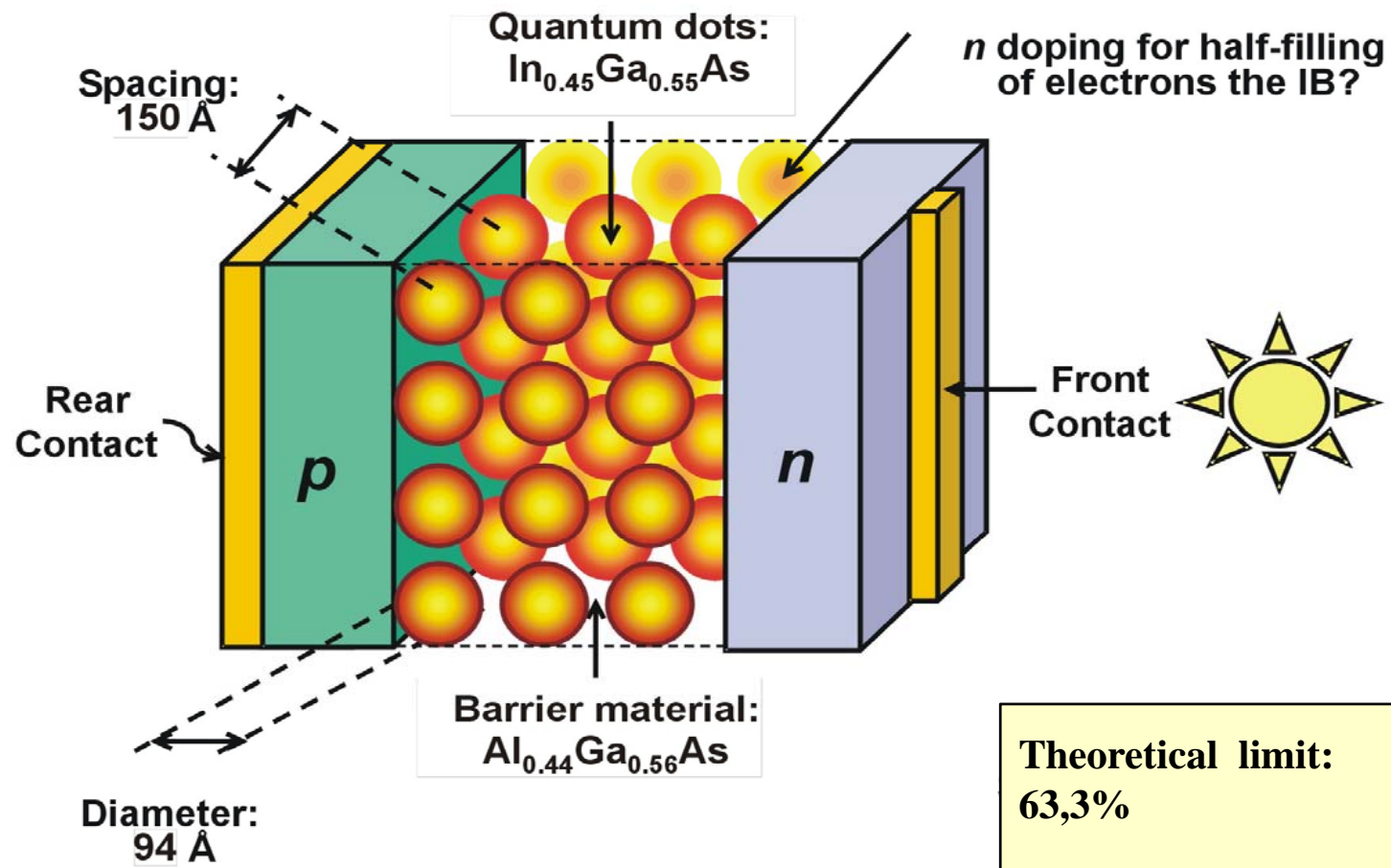


Three
junctions
 $\eta=42\%$



High efficiency approach: Third generation solar cells (II)

- Intermediate Band Solar Cell



Introduction

Crystalline silicon technology, from quartz to system

Economical and environmental issues

Alternatives to cristalline silicon technology

Conclusions



Conclusions

- **Silicon technology is dominating PV industry today**
- **Manufacturers choose the device structure reaching compromises between efficiency and fabrication cost**
- **With current technology, the energy invested in the fabrication of a PV system can be recovered in less than two years in South of Europe**
- **There are new concepts being explored which can significantly reduce the cost of the technology**
- **Photovoltaic solar energy will reach the competitive level in few years' time**



Summary of PV technologies

